

PSE Conformance Test Suite

June 28 2022 4:50 PM  
 Port Count: 12  
 Loop Count: 1  
 PSE Tested: Sample Type 2 LLDP PSE

**Sifos Technologies** 802.3at Conformance Report  
 Test Mode: 30 Watt LLDP  
 Sifos Interop Index: 95%  
 Error Log: None  
 version 5.3.00  
 report version 5.3.00

TestLoop: 1	PSA-3000 Ports										UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F		
	1-1	1-2	2-1	2-2	3-1	3-2	4-1	4-2	5-1	5-2									6-1	6-2
Test: det_v																				
Open_Circuit_Det_Vooc	6.95	7.15	7.25	6.87	6.75	6.13	6.6	6.78	7.13	6.42	7.08	6.72	V	6.13	7.25	6.8	2.8	Pass	30	Pass
Peak_Det_Vvalid	7.07	7.13	7.13	7.09	7.02	7.08	7.08	7.1	7.06	7.11	7.13	7.14	V	7.02	7.14	7.1	3.8	Pass	10	Pass
Min_Det_Vvalid	5.01	5.04	5.04	4.99	4.97	5.01	5.01	5.04	4.99	5.02	5.05	5.04	V	4.97	5.05	5	2.8	Pass	9	Pass
Det_Volt_Step_dVsteps	1.38	1.4	1.41	1.39	1.37	1.39	1.38	1.38	1.38	1.41	1.38	1.41	V	1.37	1.41	1.4	1	Pass	7.2	Pass
Detection_Slew	0	0	0	0	0	0	0	0	0	0	0	0	V/ussec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulses	3	3	3	3	3	3	3	3	3	3	3	3	edges	3	3	3	3	Pass	3	Pass
Backoff_Voltage	0.4	0.5	0.4	0.4	0.4	0.5	0.4	0.5	0.4	0.4	0.5	0.4	V	0.4	0.5	0.4	0	Pass	9	Pass
Non_802_Discr_9	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	2	Pass
Test: det_i																				
Init_Current_Isoc	0.17	0.16	0.18	0.19	0.17	0.18	0.19	0.17	0.17	0.17	0.18	0.19	mA	0.16	0.19	0.18	0	Pass	5	Pass
Det_Current_Isoc	0.14	0.14	0.16	0.15	0.15	0.16	0.15	0.15	0.15	0.15	0.16	0.17	mA	0.14	0.17	0.15	0	Pass	5	Pass
Test: det_range																				
Rgood_Max	30	29	29	29	29	29	29	29	29	29	29	29	Kohm	29	30	29.1	26	Pass	32	Pass
Rgood_Min	17	17	17	17	17	17	17	17	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rbad_Max	30	29	29	29	29	29	29	29	29	29	29	29	Kohm	29	30	29.1	26	Pass	32	Pass
Rbad_Min	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Stat	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time																				
Backoff_Time_Tdbs	488	492	492	492	484	492	500	500	492	492	523	508	msec	484	523	496.3	-1	Pass	1500	Pass
Eff_Backoff_Tdbs_eff	488	492	492	492	484	492	500	500	492	492	523	508	msec	484	523	496.3	-1	Pass	1500	Pass
Backoff_Type	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Info
Detection_Time_Tdbs	289	289	291	291	289	289	293	283	287	293	293	283	msec	287	293	290.3	5	Pass	500	Pass
Total_Det_Time	773	786	773	762	770	764	783	770	766	750	793	785	msec	750	793	770.4	5	Pass	1000	Pass
Test: det_xsource																				
PSE_Detect_Source	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Output_Impedance_Zout	450	450	450	450	450	450	450	450	450	450	450	450	Kohm	450	450	450	45	Pass	2000	Pass
Test: class_v																				
Vclass_Max	18.2	18.2	18.3	18.2	18.2	18.1	17.7	18.3	18.2	18.4	18.2	18	V	17.7	18.4	18.2	15.5	Pass	20.5	Pass
Vclass_Min	17.1	17.1	17.3	17.2	17.1	17	16.6	17.2	17.1	17.3	17.1	17	V	16.6	17.3	17.1	15.5	Pass	20.5	Pass
Test: class_time																				
Class_0_Count	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Class_4_Count	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Class_Time_Tpds	36.2	36.2	36.2	36.4	36	36.2	36.8	36.8	36	35.8	36.6	36.8	msec	35.8	36.8	36.3	5.6	Pass	75	Pass
Test: class_err																				
Class_Lime	73	75	74	74	72	73	71	75	75	75	73	73	mA	71	75	74	51	Pass	100	Pass
Pwr_Cl_Lime	4	4	4	4	4	4	4	4	4	4	4	4	****	4	4	4	1	Pass	0	Fail
Pwr_Cl_Ssr	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	0	Fail
Test: class_lddp																				
PSE_Source_Priority	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
PSE_MDI_Pwr_Sup	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
PSE_LLDP_Time_1s	1.4	1.4	1.3	1.4	1.3	1.3	1.3	1.3	1.3	1.3	1.3	1.3	sec	1.3	1.4	1.3	0	Pass	30	Pass
PSE_LLDP_Type_1s	2	2	2	2	2	2	2	2	2	2	2	2	****	2	2	2	2	Pass	2	Pass
PSE_Echo_Time_1s	1	1.7	1.7	1.7	1.3	1.6	1.6	1	2	1.7	1.6	1	sec	1	2	1.5	0	Pass	10	Pass
PSE_Alloc_Pwr_1s	8.1	8.1	8.1	8.1	8.1	8.1	8.1	8.1	8.1	8.1	8.1	8.1	W	8.1	8.1	8.1	8.1	Pass	13	Pass
PSE_Alloc_Time_1s	1.3	2	2	2	1.6	1.9	1.9	1.2	2.2	1.9	1.9	1.2	sec	1.2	2.2	1.8	0	Pass	30	Pass
PD_Power_Adjust_1s	13	13	13	13	13	13	13	13	13	13	13	13	W	13	13	13	13	Pass	13	Pass
PSE_Adjust_Time_1s	1.7	1.7	1.7	1.4	1.7	1.4	1.7	1.3	1.8	1.7	1.6	1.7	sec	1.6	1.7	1.6	0	Pass	10	Pass
Link_Down_Shutdown_7s	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
PSE_LLDP_Time_2s	1.4	1.4	1.4	1.4	1.3	1.3	1.3	1.3	1.3	1.3	1.3	1.3	sec	1.3	1.4	1.3	0	Pass	10	Pass
PSE_LLDP_Type_2s	2	2	2	2	2	2	2	2	2	2	2	2	****	2	2	2	2	Pass	2	Pass
PSE_Echo_Time_2s	1.4	1.4	1.4	1.7	1.7	1.3	1.6	1.7	1.3	1.3	1.6	1.6	sec	1.6	1.7	1.4	0	Pass	10	Pass
PSE_Alloc_Pwr_2s	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	W	20.3	20.3	20.3	20.3	Pass	25	Pass
PSE_Alloc_Time_2s	1.6	1.3	1.7	2	1.3	1.6	1.6	1.9	1.6	1.6	1.3	0.9	sec	1.3	2	1.7	0	Pass	30	Pass
PD_Power_Adjust_2s	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	W	25.5	25.5	25.5	25.5	Pass	25.5	Pass
PSE_Adjust_Time_2s	1.7	1.7	1.7	1.7	1.3	1.6	1.3	1.3	1.3	1.7	1.7	1.7	sec	1.3	2	1.6	0	Pass	10	Pass
Test: pwrup_time																				
Power-On_Trise_c0	12	12	12	9	12	12	27	30	29	26	28	29	usec	9	30	20	15	Info	50000	Pass
Power-On_Tpon_c0	183.6	164.1	183.6	166	164.1	164.1	185.6	148.4	203.1	146.5	171.9	128.9	msec	128.9	203.1	167.5	0	Pass	400	Pass
Power-On_Trise_c4	13	13	13	13	13	13	35	36	51	47	48	44	usec	13	51	28	15	Info	50000	Pass
Power-On_Tpon_c4	181.6	164.1	164.1	166	161.6	146.5	168	185.6	162.1	181.6	130.9	111.3	msec	111.3	185.6	162	0	Pass	400	Pass
Test: pwrup_inrush																				
Init_Inrush	433.13	436.63	433.63	434	434.88	441.88	436	436.63	435.65	455	435	436.88	mA	433.13	441.88	436.6	400	Pass	450	Pass
Max_Inrush	432.63	432.88	431	433.88	430.5	434.3	433	439.25	431.25	434.13	430.63	431.63	mA	430.5	439.25	434.1	400	Pass	450	Pass
Max_Inrush_c4	431.63	433.5	431.38	434.88	428.88	437.25	432.13	434	430.75	436.88	436.5	436.5	mA	428.88	437.25	433.7	400	Pass	450	Pass
Min_Inrush	426.75	426.5	424.13	424.38	421.63	428.88	424.38	427.75	424.63	427.63	431.13	429.75	mA	421.63	431.13	426.5	400	Pass	450	Pass
Tinrush	54.5	53.7	54.5	53.7	54.5	53.7	54.5	53.7	53.7	52.9	54.5	55.3	msec	52.9	55.3	54.2	50	Pass	75	Pass
Inrush_45ms	54.1	54	53.9	53.9	53.9	54	54	53.9	53.8	53.8	53.7	53.8	V	53.7	54.1	53.9	50	Pass	57	Pass
Inrush_Voltages	30.9	31.2	31.2	31.2	31.5	31.8	31.6	31.9	36.2	35.2	31	31.2	V	30.9	36.2	31.1	30	Pass	57	Pass
Inrush_Strategy_c4	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Inrush_Strategy_c4s	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: pwrup_v																				
Vport_min	53.1	53.4	52.9	53	53	53.1	52.9	53.2	52.9	52.9	52.7</									