PSE Conformance Test Suite

June 22 2019 2:00 AM

Port Count..... 24
Loop Count..... 1
PSE Tested: **Sample 24-Port Type-2 LLDP PSE**

Sifos Technologies

802.3at Conformance Report
version **5.0.07**
report version 5.0.01

Test Mode: 30 Watt LLDP
Sifos Interop Index*: 95%
Error Log: None

Chassis ID: 192.168.221.141
TestLoop: 1

PSA-3000 Ports

	1-1	1-2	2-1	2-2	3-1	3-2	4-1	4-2	5-1	5-2	6-1	6-2	7-1	7-2	8-1	8-2	9-1	9-2	10-1	10-2	11-1	11-2	12-1	12-2	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v																																
Open_Circuit_Det_Voc=	7.97	8.43	8.32	8.5	8.78	8.2	8.22	8.1	8.47	8.82	8.18	8.13	8.72	8.3	8.65	8.32	8.78	8.2	7.97	8.43	8.47	8.82	8.22	8.1	volts	7.97	8.82	8.38	2.8	Pass	30	Pass
Peak_Det_Vvalid=	7.47	7.87	7.82	7.97	8.26	7.68	7.72	7.6	7.98	8.28	7.67	7.61	8.2	7.78	8.12	7.82	8.26	7.68	7.47	7.87	7.98	8.28	7.72	7.6	volts	7.47	8.28	7.9	3.8	Pass	10	Pass
Min_Det_Vvalid=	3.58	3.83	3.8	3.93	4.07	3.7	3.74	3.68	3.88	4.04	3.74	3.67	4.01	3.77	3.97	3.81	4.07	3.7	3.58	3.83	3.88	4.04	3.74	3.68	volts	3.58	4.07	3.8	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	3.89	4.04	4.02	4.04	4.19	3.98	3.98	3.92	4.1	4.24	3.93	3.94	4.19	4.01	4.15	4.01	4.19	3.98	3.89	4.04	4.1	4.24	3.98	3.92	volts	3.89	4.24	4	1	Pass	7.2	Pass
Detection_Slew=	0.013	0.013	0.013	0.013	0.013	0.013	0.013	0.013	0.013	0.013	0.014	0.013	0.013	0.012	0.013	0.012	0.013	0.013	0.013	0.013	0.013	0.013	0.013	0.013	V/usec	0.012	0.014	0.013	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	edges	2	2	2	1	Pass	9	Pass
Backoff_Voltage=	0.1	0.1	0.1	0.2	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	volts	0.1	0.2	0.1	0	Pass	9	Pass
Non_802_Step_V=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	volts	0	0	0	0	Pass	0.1	Pass
High_Sig_MaxV=	7.46	7.96	7.89	8.04	8.34	7.76	7.79	7.67	8.04	8.32	7.75	7.64	8.27	7.86	8.21	7.89	8.34	7.76	7.46	7.96	8.04	8.32	7.79	7.67	volts	7.46	8.34	7.9	3.8	Pass	11	Pass
Non_802_Discr_?=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	2	Pass
Test: det_i																																
Init_Current_Isc=	1.25	2.69	2.91	2.87	2.58	1.98	2.41	1.93	2.78	2.74	2.01	1.75	2.66	2.78	3.02	2.86	2.58	1.98	1.25	2.69	2.78	2.74	2.41	1.93	mA	1.25	3.02	2.4	0	Pass	5	Pass
Det_Current_Isc=	1.07	0.54	0.94	1.02	1.35	0.86	0.91	1.11	0.73	1.29	1.29	0.88	1.21	0.99	1.21	0.5	1.35	0.86	1.07	0.54	0.73	1.29	0.91	1.11	mA	0.5	1.35	0.99	0	Pass	5	Pass
Test: det_range																																
Rgood_Max=	29	29	29	29	29	29	29	28	29	28	29	29	29	29	29	29	29	29	29	29	29	28	29	28	Kohm	28	29	28.8	26	Pass	32	Pass
Rgood_Min=	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	29	29	29	29	29	29	29	28	29	28	29	29	29	29	29	29	29	29	29	29	29	28	29	28	Kohm	28	29	28.8	26	Pass	33	Pass
Cgood_Max=	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time																																
Backoff_Time_Tdbo=	301	301	305	297	297	301	297	301	301	297	301	297	297	301	297	297	297	301	301	301	301	297	297	301	msec	297	305	299.3	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	301	1430.1	1405.1	1417.6	297	301	2308.6	1042.5	301	1038.6	301	297	1074.6	1135.9	297	1445.6	297	301	301	1430.1	301	1038.6	2308.6	1042.5	msec	297	2308.6	892.2	-1	Pass	1500	Info
Backoff_Type=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	76	78	76	78	92	78	76	78	76	76	88	76	88	78	82	76	92	78	76	78	76	76	76	78	msec	76	92	79.3	5	Pass	500	Pass
Total_Det_Time=	78	78	76	78	92	78	76	80	78	76	88	76	90	78	90	76	92	78	78	78	78	76	76	80	msec	76	92	80.2	5	Pass	1000	Pass
Test: det_rsource																																
Regulated_Vstep_Zout=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	KOhm	0	0	0	-0.1	Pass	12	Pass
Test: class_v																																
Class_Voltage_Vclass=	17.6	17.4	17.6	17.6	18.7	17.5	17.9	17.6	18.6	17.5	17.7	17.3	18.7	17	18.1	18	18.7	17.5	17.6	17.4	18.6	17.5	17.9	17.6	volts	17	18.7	17.8	15.5	Pass	20.5	Pass
Vclass_Min=	17.4	17.2	17.9	17.5	18.6	17.4	17.7	17.5	17.5	17.8	17.9	18.5	17.6	17.6	17.4	18.4	18.6	17.4	17.4	17.2	17.5	17.8	17.7	17.5	volts	17.2	18.6	17.7	15.5	Pass	20.5	Pass
Test: class_time																																
Event_Count=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Class_Time_Tpdc=	14.7	12.7	14.7	12.7	10.8	14.7	14.7	14.7	14.7	14.7	16.6	14.2	10.4	14.7	8.5	16.6	10.8	14.7	14.7	12.7	14.7	14.7	14.7	14.7	msec	8.5	16.6	13.8	5.6	Pass	75	Pass
Test: class_err																																
Class_lim=	62	60	61	61	62	61	62	62	62	63	61	59	61	60	62	61	62	61	62	60	62	63	62	62	mA	59	63	61	51	Pass	100	Pass
Pwr_Cl_Lim=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: class_lldp																																
PSE_Source_Priority=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_MDI_Pwr_Sup=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_LLDP_Time_2=	**15.8**	**15.8**	**15.8**	**15.8**	**15.6**	**15.6**	**15.8**	**15.8**	**15.9**	**15.9**	**15.8**	**15.8**	**15.5**	**15.6**	**15.8**	**15.8**	**15.6**	**15.6**	**15.8**	**15.8**	**15.9**	**15.9**	**15.8**	**15.8**	sec	15.5	15.9	15.8	0	Pass	10	**Fail**
PSE_LLDP_Type_2=	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	*	2	2	2	2	Pass	2	Pass
PSE_Echo_Time_2=	**11.9**	**11.9**	**23**	**23.9**	**11.9**	**11.9**	**22.6**	**23**	**11.9**	**11.9**	**23**	**23**	**11.9**	**23**	**23.9**	**11.9**	**11.9**	**11.9**	**11.9**	**11.9**	**11.9**	**11.9**	**22.6**	**23**	sec	11.9	23.9	16.6	0	Pass	10	**Fail**
PSE_Alloc_Pwr_2=	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	Watts	20.3	20.3	20.3	20.3	Pass	25.5	Pass
PSE_Alloc_Time_2=	12.2	12.2	23.3	24.2	12.1	12.2	22.9	23.3	12.2	12.2	23.3	23.3	12.2	12.3	23.3	24.2	12.1	12.2	12.2	12.2	12.2	12.2	22.9	23.3	sec	12.1	24.2	16.9	0	Pass	30	Pass
PD_Power_Adjust_2=	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	Watts	25.5	25.5	25.5	25.5	Pass	25.5	Pass
PSE_Adjust_Time_2=	**11.1**	**11**	**11.2**	**11.2**	**11**	**11.5**	**11.2**	**11.2**	**11.5**	**11.5**	**11.2**	**11.2**	**11**	**11.5**	**11.2**	**11.7**	**11**	**11.5**	**11.1**	**11**	**11.5**	**11.5**	**11.2**	**11.2**	sec	11	11.7	11.3	0	Pass	10	**Fail**
Link_Down_Shutdown_?=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		0	0	0	0	Pass	0	Pass
Test: pwrup_time																																
Pwr-On_Rise_Time_Trise=	89	90	97	97	95	89	159	145	94	81	136	141	87	77	152	140	95	89	89	90	94	81	159	145	usec	77	159	109	15	Pass	50000	Pass
Power-On_Time_Tpon=	335.9	330.1	373.1	**408.2**	322.3	322.3	**487.5**	326.2	324.2	322.3	**414.1**	355.5	322.3	**414.1**	**419.9**	355.5	322.3	322.3	335.9	330.1	324.2	322.3	**514.6**	326.2	msec	322.3	514.6	359.6	0	Pass	400	**Fail**
Test: pwrup_inrush																																
Init_Iinrush=	423.25	427.25	422.75	422.13	415.75	425.25	419.25	423.38	417	428.13	417.38	419.13	412.88	419	423.25	428.25	415.75	425.25	423.25	427.25	417	428.13	419.25	423.38	mA	412.88	428.25	421.8	400	Pass	450	Pass
Max_Iinrush_c4=	426	428.5	425.63	422.75	416.5	427.5	420.38	424.25	417	428.88	418.75	418.88	414.38	419.38	425.75	430.13	416.5	427.5	426	428.5	417	428.88	420.38	424.25	mA	414.38	430.13	423.1	400	Pass	450	Pass
Min_Iinrush=	420.75	423.25	419.88	417.63	411.38	422.38	415.13	419.38	411.63	423.25	413.5	413.5	409.25	414.25	420.25	425.38	411.38	422.38	420.75	423.25	411.63	423.25	415.13	419.38	mA	409.25	425.38	417.8	400	Pass	450	Pass
Tinrush=	63.9	62.3	65	63.1	64.6	68.6	68.9	61.9	66.2	62.7	63.1	62.7	62.7	61.1	68.9	61.9	64.6	68.6	63.9	62.3	66.2	62.7	68.9	61.9	msec	61.1	68.9	64.4	50	Pass	75	Pass
Inrush_45m=	53.6	53.5	53.5	53.5	53.5	53.5	53.5	53.4	53.5	53.5	53.3	53.4	53.5	53.5	53.2	53.4	53.5	53.5	53.6	53.5	53.2	53.5	53.5	53.4	V/usec	53.2	53.6	53.5	50	Pass	57	Pass
Inrush_Voltage=	30.7	31.1	31.5	31.4	30.7	31	30.7	30.8	30.1	30.5	30.5	30.6	31	31.3	30.6	30.9	30.7	31	30.7	31.1	30.1	30.5	30.7	30.8	Volts	30.1	31.5	30.8	30	Pass	57	Pass
Max_Init_Inrush=	583.3	520	503.5	503	577.5	527	422.5	428	562	572	422	421.8	502.8	503	427	432.3	577.5	527	583.3	520	562	572	422.5	428	mA	421.8	583.3	504.2	0	Pass	2000	Pass
Inrush_Strategy_c4=	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	****	2	2	2	0	Pass	1	Info
Test: pwron_v																																
Vport_min_2=	52.7	52.6	52.5	52.6	52.6	52.5	52.6	52.5	52.6	52.5	52.4	52.5	52.5	52.6	52.3	52.5	52.6	52.5	52.7	52.6	52.6	52.5	52.6	52.5	V	52.3	52.7	52.5	50	Pass	57	Pass
Vport_max_2=	53.8	53.8	53.7	53.7	53.7	53.7	53.7	53.7	53.7	53.7	53.5	53.6	53.7	53.7	53.5	53.6	53.7	53.7	53.8	53.8	53.7	53.7	53.7	53.7	V	53.5	53.8	53.7	50	Pass	57	Pass
Vport_ripple_2=	24	24	23	22	23	58	23	22	57	56	57	42	53	56	57	57	23	58	24	24	57	56	23	22	mVpp	22	58	39.2	0	Pass	500	Pass
Vport_noise_2=	167	163	163	158	146	153	158	168	155	171	149	165	149	163	149	162	158	169	169	152	163	146	147	158	mVpp	146	171	158.4	0	Pass	200	Pass
Vtrans_min_2=	52.4	52.4	52.4	52.3	52.4	52.2	52.4	52.4	52.3	52.4	52.2	52.3	52.3	52.4	52	52.3	52.4	52.2	52.4	52.4	52.3	52.4	52.4	52.4	V	52	52.4	52.3	50	Pass	57	Pass
Vtrans_max_2=	53.8	53.9	53.9	53.7	53.9	53.9	53.8	53.9	53.8	53.9	53.7	53.8	53.8	53.7	53.5	53.7	53.9	53.9	53.8	53.9	53.8	53.9	53.8	53.9	V	53.5	53.9	53.8	50	Pass	57	Pass
Test: pwron_pwrcap																																
Pcon_c4=	30.1	30.2	30.1	30.1	30.4	30.2	30.5	30.1	30.4	30.2	29.9	30.1	30.1	29.8	29.5	29.9	30.4	30.2	30.1	30.2	30.4	30.2	30.5	30.1	watts	29.5	30.5	30.2	28.7	Pass	38.9	Pass
Icon_%_c4=	102.7	102.8	102.4	102.6	103.5	102.6	103.7	102.5	103.4	102.6	101.6	102.4	102.6	101.4	100.2	101.6	103.5	102.6	102.7	102.8	103.4	102.6	103.7	102.5	%	100.2	103.7	102.6	100	Pass	9999	Pass
Type-2_Enable=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pclass_LLDP_22.7=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Pclass_LLDP_24.5=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi																																
Ilim_Peak=	470	543.5	496.8	520	569.3	533.3	585.3	539	512.3	481.8	390.8	463.8	426.8	442.8	523.5	434	569.3	533.3	470	543.5	512.3	481.8	585.3	539	mA	390.8	585.3	507	0	Pass	1750	Pass
Ilim_Min_2=	686	686.5	685.6	685.5	685.4	686.7	686.8	685.4	685.5	686	686.5	685.5	686.2	685.5	685.5	687.3	685.4	686.7	686	686.5	685.5	686	686.8	685.4	mA	685.4	687.3	686	683	Pass	1750	Pass
Tlim_2=	59.4	62.5	61.7	62.9	60.5	62.1	60.2	59	60.5	59.4	61.3	60.2	58.2	62.1	64.8	64.8	60.5	62.1	59.4	62.5	60.5	59.4	60.2	59	msec	58.2	64.8	61	10	Pass	75	Pass
Vlim_2=	52.3	52.3	52.2	52.2	52.3	52.1	52.2	52.2	52.3	52.2	52.1	52.2	52.2	52.3	52	52.2	52.3	52.1	52.3	52.3	52.3	52.2	52.2	52.2	V	52	52.3	52.2	50	Pass	57	Pass
Ilim_Max_2=	839.5	861.5	849	844.8	860.8	824.8	861.5	849.5	843.8	857.5	840	847	852.5	846.5	859.5	835.3	860.8	824.8	839.5	861.5	843.8	857.5	861.5	849.5	mA	824.8	861.5	848.9	0	Pass	1750	Pass
Ilim_Low_V_Tol_2=	6.6	6.3	6.6	5.5	7.8	6.6	5.5	6.6	8.6	8.2	8.2	5.1	6.3	6.3	5.1	5.9	7.8	6.6	6.6	6.3	8.6	8.2	5.5	6.6	msec	5.1	8.6	6.7	10	Info	9999	Pass
Xtran_lo_2=	104.2	104.2	104.3	104.1	104.4	103.9	104.3	104.1	104.3	104	103.9	104	104.2	104.4	103.6	104.1	104.4	103.9	104.2	104.2	104.3	104	104.3	104.1	%	103.6	104.4	104.1	92.4	Pass	115	Pass
Test: pwron_overld																																
%Ipeak_2=	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_2=	52.3	52.3	52.2	52.2	52.3	52.1	52.2	52.2	52.3	52.2	52.1	52.2	52.3	52.3	52	52.1	52.3	52.1	52.3	52.3	52.3	52.2	52.2	52.2	V	52	52.3	52.2	50	Pass	57	Pass
Vport_5%DC_2=	52.4	52.3	52.2	52.3	52.3	52.1	52.3	52.2	52.3	52.2	52.1	52.3	52.3	52.3	52	52.1	52.3	52.1	52.4	52.3	52.3	52.2	52.3	52.2	V	52	52.4	52.2	50	Pass	57	Pass
Test: mps_dc_valid																																
Min_Valid_Time_Tmps=	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn																																
Min_Valid_I_hold=	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	mA	8	8	8	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	363	367	363	367	375	367	367	367	363	363	363	367	363	371	367	367	375	367	363	367	363	363	367	367	msec	363	375	366	300	Pass	400	Pass
Max_Voltage_Vopen_max=	0	0	8.3	8.5	0	0	0	0	0	8.8	0	8.1	0	0	0.8	0	0	0	0	0	0	8.8	0	0	volts	0	8.8	1.8	-1	Pass	30	Pass
Test: pwrdn_overld																																
Icut_2=	670	670	670	670	670	659	659	659	659	659	659	659	659	659	659	659	670	659	670	670	659	659	659	659	mA	659	670	662.7	-1	Pass	1750	Pass
Tcut_2=	58.6	58.6	66.4	58.6	66.4	66.4	74.2	66.4	58.6	58.6	66.4	66.4	58.6	66.4	58.6	58.6	66.4	66.4	58.6	58.6	58.6	58.6	74.2	66.4	msec	58.6	74.2	63.2	10	Pass	9999	Pass
Isoft_2=	649.3	649.3	649.3	649.3	649.3	638.3	638.3	638.3	638.3	638.3	638.3	638.3	638.3	638.3	638.3	638.3	649.3	638.3	649.3	649.3	638.3	638.3	638.3	638.3	mA	638.3	649.3	642	-1	Pass	683	Pass
Tsoft_2=	1089.8	1066.4	972.7	1019.5	941.4	1097.7	1097.7	1003.9	1082	1027.3	1019.5	1019.5	1113.3	957	1066.4	1043	941.4	1097.7	1089.8	1066.4	1082	1027.3	1097.7	1003.9	msec	941.4	1113.3	1042.6	-1	Pass	2000	Pass
Test: pwrdn_time																																
Turn-Off_Time_Toff=	65.8	66.6	70.4	72	69	69.7	67.4	69.3	74.2	68.5	73	65.9	72	73.5	68.3	70.7	69	69.7	65.8	66.6	74.2	68.5	67.4	69.3	mSec	65.8	74.2	69.5	0	Pass	500	Pass
Output_Cap_Cout=	0.0813	0.0829	0.0881	0.0913	0.0864	0.0874	0.084	0.0866	0.0942	0.0858	0.0924	0.0811	0.0905	0.0935	0.0855	0.0892	0.0864	0.0874	0.0813	0.0829	0.0942	0.0858	0.084	0.0866	uF	0.0811	0.0942	0.09	-1	Pass	0.52	Pass
Output_Load_Rp=	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	9999	Kohm	9999	9999	9999	45	Pass	50000	Pass
Test: pwrdn_v																																
Avg_Idle_Voff=	0.1	0.1	0.1	0.2	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	VDC	0.1