PD Conformance Test Suite Parameters

Note: the parameter suffix '_x' indicates this value is independently measured for Alt A and Alt B. The reported parameters will use the suffix '_A' or '_B'.

Parameter	Description	Units	Acceptance Criteria (802.3bt references)	PICS
Detection				
Rdet_x	Detection resistance (V_{PD} in 2.7 to 10.1 V range) from a single probe.	kΩ	23.7 to 26.3 kΩ 802.3bt Table 145-21	PD2 PD9
Rdet_final_x	Detection resistance after multiple detection and classification probing sequences. Evaluate response to PSE's that probe repeatedly prior to power-up.			PD10 PD11 PD12
Rdet_unpwr_x	Non-valid detection resistance presented by the unpowered pairset of a Single signature PD.	kΩ	< 12 or > 45 kΩ 802.3bt section 145.3.4, Table 145-22	PD13 PD14
Rdet_at_Vmin_x	Detection resistance measured using the chord V_{PD} = 2.7 to 4.2 V.	kΩ	23.7 to 26.3 kΩ 802.3bt Table 145-21	PD15 PD16
Rdet_at_Vmax_x	Detection resistance measured using the chord V_{PD} = 7.0 to 10.0 V.			PD17
Rdet_Voffset_x	PD detection signature offset.	VDC	0 to 1.9 VDC 802.3bt Table 145-21, Figure 145-29	PD18 PD19
SigType	PD signature type.	-	SINGLE or DUAL (user declaration) 802.3bt section 145.3.5	
Cdet_x	Detection capacitance (V_{PD} in 2.7 to 10.1 V range)	µF	0.05 to 0.12 µF 802.3bt Table 145-21	
Cdet_final_x	Detection capacitance after multiple detection and classification probing sequences. Evaluate response to PSE's that probe repeatedly prior to power-up.			
Classification				
ClassNum ClassNum_x	PD Class determined from a 5 Event classification.	PD Class	1, 2, 3, 4, 5, 6, 7, 8 (SINGLE sig.) (user declaration) 1D, 2D, 3D, 4D, 5D (DUAL sig.) (user declaration) 802.3bt section 145.3.6, Tables 145-26 and 145-27	PD22 PD24 PD25
class_sig_EV1-2_min class_sig_EV1-2_min_x	Minimum current drawn during Event 1 or 2 (class_sig_A).	mA	1-4, 9-12, 17-20, 26-30, or 36-44 802.3bt Table 145-24	PD27 PD28 PD29
class_sig_EV1-2_max class_sig_EV1-2_max_x	Maximum current drawn during Event 1 or 2 (class_sig_A).	mA	1-4, 9-12, 17-20, 26-30, or 36-44 802.3bt Table 145-24	PD30 PD31
class_sig_EV3-5_min class_sig_EV3-5_min_x	Minimum current drawn during Event 3, 4, or 5 (class_sig_B).	mA	1-4, 9-12, 17-20, 26-30, or 36-44 802.3bt Table 145-24	PD32 PD33
class_sig_EV3-5_max class_sig_EV3-5_max_x	Maximum current drawn during Event 3, 4, or 5 (class_sig_B).	mA	1-4, 9-12, 17-20, 26-30, or 36-44 802.3bt Table 145-24	PD34 PD35
MarkI MarkI_x	Current drawn during mark region of a 2-Event 5-Event classification.	mA	0.25 to 4 mA 802.3bt Table 145-25	
Tclass_max Tclass_max_x	Time from when V_{PD} = 15.5VDC until class current reaches valid level. Maximum of the times observed for Events 1..5.	ms	< 5.0 ms 802.3bt 145.3.6.1 & Table 145-25	
Iclass_EV1_at_Vmin Iclass_EV1_at_Vmin_x	Classification current signature at V_{Class_PD} 14.5V. (single event)	mA	1-4, 9-12, 17-20, 26-30, or 36-44 802.3bt Table 145-24, Table 145-25	
Iclass_EV1_at_Vmax Iclass_EV1_at_Vmax_x	Classification current signature at V_{Class_PD} 20.5V. (single event)	mA	1-4, 9-12, 17-20, 26-30, or 36-44 802.3bt Table 145-24, Table 145-25	
Class_Reset Class_Reset_x	Flag indicating PD correctly reverted to IDLE and presented the expected class signatures class_sig_A and class_sig_B following V_{PD} < V_{Reset_th}.	flag (1 or 0)	A value of 1 indicates PD correctly reset. 802.3bt section 145.3.6.1.1	
Autoclass	Flag indicating PD class signature behavior during Event 1 matches user declaration. (optional behavior, SINGLE sig. PDs only)	flag (1 or 0)	P or F based on PD behavior vs. user declaration. 802.3bt section 145.3.6.2	
Tacs	Time during Event 1 that the PD changes to class signature 0. (optional behavior, SINGLE sig. PDs only)	ms	75.5 to 87.5ms 802.3bt Table 145-28	
Power-Up / Down				
Iinrush_init	Highest current drawn during the first 50 msec after power on (4 pair).	mA	< Class specific Max limits from Table 145-29. 802.3bt Table 145-29 & section 145.3.8.3.	PD8 PD51
Iinrush_tdel	Highest current drawn during the period from 50 msec to 80 msec after power-up (4 pair).	mA		PD52 PD53
Iinrush_init_x	Highest current drawn during the first 50 msec after power on (2 pair).	mA		
Iinrush_tdel_x	Highest current drawn during the period from 50 msec to 80 msec after power-up (2 pair).	mA		
Tinrush	Time when inrush will be satisfied (4 pair).	ms	Computed from the total energy based on VPort and current drawn during a 50ms interval starting at the first current sample > Iinrush_PD(max) occurring during the first 50ms after power has been applied. If the DUT meets the defined inrush behavior, will be reported as 50ms and will pass. If the DUT exceeds 14.4W peak draw between 50ms . 80ms, will be reported as 80ms and will fail. PD's that never exceed Inrush_PD(max) will be reported as 50msec and will pass.	
Tinrush_x	Time when inrush will be satisfied (2 pair).	ms		
IlimMinViolation	PSE will set I_{LIM-2P} to 400mA for assigned class 1-3, which is the assigned class established by the test conditions. DUT is checked over a 1.5 second interval after T_{Inrush} has completed.	flag (1 or 0)	802.3bt Tables 145-29 & 145-16.	PD52 PD53
Vrefl_x	Voltage developed across a 100k load, connected for SINGLE or DUAL sig PD as shown in Figure 145-31.	VDC	< 2.8 VDC 802.3bt Table 145-29 & section 145.3.8.8.	PD7 PD66 PD67
Von	Voltage at which PD starts to draw load current	VDC	30 to 42, 802.3bt Table 145-29	PD39
Voff	Voltage at which PD stops drawing load current	VDC	> 30, 802.3bt Table 145-29. WARN if > 37VDC.	PD41
Vhyst	Hysteresis band between **Von** and **Voff**.	VDC	Provided for information purposes. There is no explicit value for hysteresis specified in Table 145-29, but the need for hysteresis is implied by 802.3bt section 145.3.8.1 (startup oscillation).	PD43
2 Pair Powered Type-1 PHY	*PD-under-test powered to PHY max Vport with 1-Event Classification*			
Minl_1	Minimum current drawn by the PD while powered on at Type-1 V_{Port_PD}	mA	0 to (P_{Class_PD} / V_{PD}) 802.3bt section 145.3.8.9, Table 145-31	PD1 PD3 PD4 PD5
Vport_1	Vport level at the point where the **Ipeak_1** occurs.	V	V_{Port_PD} should conform to ranges in 802.3bt Table 145-29	PD6 PD9 PD20
Ppeak_1	Maximum instantaneous power consumed by the PD while powered on at Type-1 V_{Port_PD}	W	< P_{Peak_PD} (for PD Classification) 802.3bt Table 145-29, section 145.3.8.4	PD21 PD38
Pport_1	Average power (1 second moving window) consumed by the PD while powered on a Type-1 V_{Port_PD}	W	< P_{Class_PD} (for PD Classification) 802.3bt Tables 145-29,-1,and-26, section 145.3.8.2, 145.3.6	PD44 PD54
PeakViolation_1	Flag indicating that PD power draw exceeded P_{Peak_PD}. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	802.3bt section 145.3.8.4	PD72 PD73
MPSViolation_1	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > I_{Port_MPS} for a time period > T_{MPS_PD} msec out of every T_{MPDO_PD} msec time interval. 802.3bt section 145.3.9, Table 145-32	
TcutWindowViolation_1	Flag indicating that PD power draw exceeded P_{Class_PD} for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} for time duration > T_{CUT} min, or 50 msec. 802.3bt section 145.3.8.2 (also see 145.2.10.9).	
DutyCycleViolation_1	Flag indicating that PD power draw is exceeding P_{Class_PD} for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} for > 5% duty cycle. 802.3bt section 145.3.8.4.	
2 Pair Powered Type-3 PHY	*PD-under-test powered to PHY min Vport using 2-Event Classification*			
Minl_2	Minimum current drawn by the PD while powered on at Type-2 V_{PD}	mA	0 to (P_{Class_PD} / V_{PD}) 802.3bt section 145.3.8.9, Table 145-31	PD1 PD3 PD4 PD5
Vport_2	Vport level at the point where the Ipeak_2 occurs.	V	V_{Port_PD} should conform to ranges in 802.3bt Table 145-29	PD6 PD9 PD20
Ppeak_2	Maximum instantaneous power consumed by the PD while powered on at Type-2 V_{Port_PD}	W	< P_{Peak_PD} (for PD Classification) 802.3bt Table 145-29, section 145.3.8.4	PD21 PD38 PD44

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Parameter	Description	Units	Acceptance Criteria (802.3bt references)	PICS
Pport_2	Average power (1 second moving window) consumed by the PD while powered on a Type-2 V_{Port_PD}	W	< P_{Class_PD} (for PD Classification) 802.3bt Tables 145-29,-1,and-26, section 145.3.8.2, 145.3.6	PD44 PD54 PD72 PD73
PeakViolation_2	Flag indicating that PD power draw exceeded P_{Peak_PD}. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	802.3bt section 145.3.8.4	
MPSViolation_2	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > I_{Port_MPS} for a time period > T_{MPS_PD} msec out of every T_{MPDO_PD} msec time interval. 802.3bt section 145.3.9, Table 145-32	
TcutWindowViolation_2	Flag indicating that PD power draw exceeded P_{Class_PD} for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} for time duration > T_{CUT} min, or 50 msec. 802.3bt section 145.3.8.2 (also see 145.2.10.9).	
DutyCycleViolation_2	Flag indicating that PD power draw is exceeding P_{Class_PD} for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} for > 5% duty cycle. 802.3bt section 145.3.8.4.	
4 Pair Powered Type-3 PHY	*PD-under-test powered to PHY min Vport (full power alloc) or PHY max Vport (demotion) with N-Event Classification, N determined by advertised class.*			PD1 PD3
MinI_3	Minimum current drawn by the PD while powered on at Type-3 V_{Port_PD-2P}	mA	0 to (P_{Class_PD} / V_{PD}) 802.3bt section 145.3.8.9, Table 145-31	PD4 PD5 PD6 PD9
Vport-2P_3_x	Vport level at the point where the peak draw occurs.	V	V_{Port_PD} should conform to ranges in 802.3bt Table 145-29	PD10 PD20
Ppeak_3	Maximum instantaneous power P_{Peak_PD} consumed by the PD while powered on at Type-3 V_{Port_PD-2P} (SINGLE sig)	W	< P_{Peak_PD} (SINGLE) or P_{Peak_PD-2P} (DUAL) for the assigned class 802.3bt Table 145-29, section 145.3.8.4	PD21 PD36
Ppeak-2P_3_x	Maximum instantaneous power P_{Peak_PD-2P} consumed by the PD powered on at Type-3 V_{Port_PD-2P} (DUAL sig)			PD38 PD44
Pport_3	Average power (1 second moving window) consumed by the PD while powered on a Type-3 V_{Port_PD-2P} (SINGLE sig)	W	< P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for the assigned class 802.3bt Tables 145-29,-1,and-26, section 145.3.8.2, 145.3.6	PD45 PD46
Pport-2P_3_x	Average power (1 second moving window) consumed by the PD while powered on a Type-3 V_{Port_PD-2P} (DUAL sig)			PD54 PD55
PeakViolation_3 PeakViolation_3_x	Flag indicating that PD power draw exceeded P_{Peak_PD} (SINGLE) or P_{Peak_PD-2P} (DUAL). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	802.3bt section 145.3.8.4	PD72 PD73 PD74
MPSViolation_3 MPSViolation_3_x	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > I_{Port_MPS} (SINGLE) or I_{Port_MPS-2P} (DUAL) for a time period > T_{MPS_PD} msec out of every T_{MPDO_PD} msec time interval. 802.3bt section 145.3.9, Table 145-32	PD75 PD77 **only if MPS low pwr PD37
TcutWindowViolation_3 TcutWindowViolation_3_x	Flag indicating that PD power draw exceeded P_{Class_PD} for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for time duration > T_{CUT} min, or 50 msec. 802.3bt section 145.3.8.2 (also see 145.2.10.9).	
DutyCycleViolation_3 DutyCycleViolation_3_x	Flag indicating that PD power draw is exceeding P_{Class_PD} for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for > 5% duty cycle. 802.3bt section 145.3.8.4.	
4 Pair Powered Type-4 PHY	*PD-under-test powered to PHY min Vport with N-Event Classification, N determined by advertised class.*			PD1 PD3
MinI_4	Minimum current drawn by the PD while powered on at Type-4 V_{Port_PD-2P}	mA	0 to (P_{Class_PD} / V_{PD}) 802.3bt section 145.3.8.9, Table 145-31	PD4 PD5 PD6 PD9
Vport-2P_4_x	Vport level at the point where the peak draw occurs.	V	V_{Port_PD} should conform to ranges in 802.3bt Table 145-29	PD10 PD20
Ppeak_4	Maximum instantaneous power P_{Peak_PD} consumed by the PD while powered on at Type-4 V_{Port_PD-2P} (SINGLE sig)	W	< P_{Peak_PD} (SINGLE) or P_{Peak_PD-2P} (DUAL) for the assigned class 802.3bt Table 145-29, section 145.3.8.4	PD21 PD36
Ppeak_2P_4_x	Maximum instantaneous power P_{Peak_PD-2P} consumed by the PD while powered on at Type-4 V_{Port_PD-2P} (DUAL sig)			PD38 PD44
Pport_4	Average power (1 second moving window) P_{Class_PD} consumed by the PD while powered on a Type-4 V_{Port_PD-2P} (SINGLE sig)	W	< P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for the assigned class 802.3bt Tables 145-29,-1,and-26, section 145.3.8.2, 145.3.6	PD45 PD46
Pport-2P_4_x	Average power (1 second moving window) consumed by the PD while powered on a Type-4 V_{Port_PD-2P} (DUAL sig)			PD54 PD55
PeakViolation_4 PeakViolation_4_x	Flag indicating that PD power draw exceeded P_{Peak_PD} (SINGLE) or P_{Peak_PD-2P} (DUAL). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	802.3bt section 145.3.8.4	PD72 PD73 PD74
MPSViolation_4 MPSViolation_4_x	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > I_{Port_MPS} (SINGLE) or I_{Port_MPS-2P} (DUAL) for a time period > T_{MPS_PD} msec out of every T_{MPDO_PD} msec time interval. 802.3bt section 145.3.9, Table 145-32	PD75 PD77 **only if MPS low pwr PD37
TcutWindowViolation_4 TcutWindowViolation_4_x	Flag indicating that PD power draw exceeded P_{Class_PD} for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for time duration > T_{CUT} min, or 50 msec. 802.3bt section 145.3.8.2 (also see 145.2.10.9).	
DutyCycleViolation_4 DutyCycleViolation_4_x	Flag indicating that PD power draw is exceeding P_{Class_PD} for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for > 5% duty cycle. 802.3bt section 145.3.8.4.	
4 Pair Powered LLDP	*PD-under-test powered to LLDP Vport with 1-Event Classification and subsequent LLDP Negotiation*			all 4pair PHY and
MinI_PreAlloc	Minimum current drawn by the PD while powered on at Type-4 V_{Port_PD-2P}	mA	0 to (P_{Class_PD} / V_{PD}) 802.3bt section 145.3.8.9, Table 145-31	PD23 PD47
Vport-2P_PreAlloc_x	Vport level at the point where the peak draw occurs.	V	V_{Port_PD} should conform to ranges in 802.3bt Table 145-29	PD48
Ppeak_PreAlloc	Maximum instantaneous power P_{Peak_PD} consumed by the PD while powered on at Type-4 V_{Port_PD-2P} (SINGLE sig)	W	< P_{Peak_PD} (SINGLE) or P_{Peak_PD-2P} (DUAL) for the assigned class 802.3bt Table 145-29, section 145.3.8.4	
Ppeak_PreAlloc_x	Maximum instantaneous power P_{Peak_PD-2P} consumed by the PD while powered on at Type-4 V_{Port_PD-2P} (DUAL sig)			
Pport_PreAlloc	Average power (1 second moving window) P_{Class_PD} consumed by the PD while powered on a Type-4 V_{Port_PD-2P} (SINGLE sig)	W	< P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for the assigned class 802.3bt Tables 145-29,-1,and-26, section 145.3.8.2, 145.3.6	
Pport_PreAlloc_x	Average power (1 second moving window) P_{Class_PD-2P} consumed by the PD while powered on a Type-4 V_{Port_PD-2P} (DUAL sig)			
PeakViolation_PreAlloc PeakViolation_PreAlloc_x	Flag indicating that PD power draw exceeded P_{Peak_PD} (SINGLE) or P_{Peak_PD-2P} (DUAL). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	802.3bt section 145.3.8.4	
MPSViolation_PreAlloc MPSViolation_PreAlloc_x	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > I_{Port_MPS} (SINGLE) or I_{Port_MPS-2P} (DUAL) for a time period > T_{MPS_PD} msec out of every T_{MPDO_PD} msec time interval. 802.3bt section 145.3.9, Table 145-32	
TcutWindowViolation_PreAlloc TcutWindowViolation_PreAlloc_x	Flag indicating that PD power draw exceeded P_{Class_PD} for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for time duration > min, or 50 msec. 802.3bt section 145.3.8.2 (also see 145.2.10.9).	
DutyCycleViolation_PreAlloc DutyCycleViolation_PreAlloc_x	Flag indicating that PD power draw is exceeding P_{Class_PD} for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed P_{Class_PD} (SINGLE) or P_{Class_PD-2P} (DUAL) for > 5% duty cycle. 802.3bt section 145.3.8.4.	
MinI_PostAlloc Vport-2P_PostAlloc_x Ppeak_PostAlloc Ppeak_PostAlloc_x Pport_PostAlloc Pport_PostAlloc_x	These parameters have to conform to the same conditions as defined above for the PreAlloc parameters, with the difference being that the assigned class			see above

Note: the parameter suffix '_x' indicates this value is independently measured for Alt A and Alt B. The reported parameters will use the suffix '_A' or '_B'.

Parameter	Description	Units	Acceptance Criteria (802.3bt references)	PICS
PeakViolation_PostAlloc	These parameters have to conform to the same conditions as defined above for the _PreAlloc parameters, with the difference being that the assigned class and therefore P_{Class_PD} and P_{Peak_PD} (SINGLE) or P_{Class_PD-2P} and P_{Peak_PD-2P} (DUAL), have changed to correspond to the requested power from the PD, which has been allocated by the DLL process running in the PDA-604. Reference 802.3bt section 145.5.			
PeakViolation_PostAlloc_x				
MPSViolation_PostAlloc				
MPSViolation_PostAlloc_x				
TcutWindowViolation_PostAlloc				
TcutWindowViolation_PostAlloc_x				
DutyCycleViolation_PostAlloc				
DutyCycleViolation_PostAlloc_x				
PD LLDP Protocol Parameters	*PD-under-test powered to Type-4 Vport with 1-Event Classification and subsequent LLDP Negotiation*			
TimeToLink	Length of time from power-up until LAN link was established. Reports±1qif no link established.	seconds	Information parameter (INFO).	DLL1 DLL2 DLL3 DLL6
LinkSpeed	Speed of the LAN link. Reports ±1qif no link established.	10 | 100 | 1000	Information parameter (INFO).	DLL8
FirstReqTime	Length of time after power-up until first LLDP Power-via-MDI request (SINGLE sig) or PD requested power value Mode A (DUAL sig) received.	sec	Information parameter (INFO). 802.3bt requires that a Type-2 PD should link and initiate LLDP within 5 minutes of power-up. 802.3bt section 145.5.2	DLL10 DLL11 DLL13
FirstReqTime_B	Length of time after power-up until first LLDP Power-via-MDI PD requested power value Mode B received.	sec	Information parameter (INFO). 802.3bt requires that a Type-2 PD should link and initiate LLDP within 5 minutes of power-up. 802.3bt section 145.5.2	
PowerRequest	Contents of the PD requested power value field (SINGLE sig) or PD requested power value Mode A (DUAL sig) in the Power-via-MDI TLV.	W	0.1 to 99.9 W (SINGLE sig) 0.1 to 49.9 W (DUAL sig) 802.3 section 79.3.2.5,79.3.2.6	
PowerRequest_B	Contents of the PD requested power value Mode B field in the Power-via-MDI TLV.	W	0.1 to 49.9 W (DUAL sig) 802.3 section 79.3.2.5,79.3.2.6	
PDAckTime	Length of time it takes the PD to transmit of an updated LLDPDU after an LLDPDU with a new PSE allocated power value (SINGLE sig) or PSE allocated power value Alternative A (DUAL sig) has been sent by the PSE (PDA-604).	sec	< 10 sec 802.3bt section 145.5.2	
PDAckTime_B	Length of time it takes the PD to transmit of an updated LLDPDU after an LLDPDU with a new PSE allocated power value Alternative B (DUAL sig) has been sent by the PSE (PDA-604).	sec	< 10 sec 802.3bt section 145.5.2	
AllocPowerEchoed	Contents of the PSE allocated power value (SINGLE sig) or PSE allocated power value Alternative A (DUAL sig) from the LLDPDU acknowledgement sent by the PD.	W	= Allocated power value sent by PDA-600. this will be same as **PowerRequest** so long as **PowerRequest** is valid value. 802.3bt section 145.5.3.3, 145.5.3.4	
AllocPowerEchoed_B	Contents of the PSE allocated power value Alternative B (DUAL sig) from the LLDPDU acknowledgement sent by the PD.	W	= Allocated power value sent by PDA-600. this will be same as **PowerRequest** so long as **PowerRequest** is valid value. 802.3bt section 145.5.3.3, 145.5.3.4	
RespondsToAT_TLV	Flag indicating whether or not the PD responded to a 12 octet Power via MDI TLV. 802.3cv maintenance adds a recommendation that the PD respond AFTER it has first responded to a 29 octet Power via MDI TLV.		Information parameter (INFO). The response behavior is recommended, but is not a formal (normative) requirement.	