PDA-600 AT TEST REPORT

December 3, 2021 3:08 PM

Product Tested: Cycles:

Sample Class 4 PD 2

Coverage:	ALT A	MDI	Type-1
	ALT B	MDI-X	Type-2 PHY
Color Key:			Type-2 LLDP
PASS	FAIL	WARN	INFO

Software Version: 2.3.0.2
PDA Firmware: 2.08
Report Ver: 2.10
Serial Number 602B0161

Sifos® Technologies

Detection & Classification

PSE Emulation: Pairs: A Polarity: MDI-X Det_Cycles: 3

Parameter	Cycle: 1	2	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
Rdet	25.38	25.43	kohm	25.38	25.43	25.41	23.70	26.30	P
Rdet_final	25.45	25.38	kohm	25.38	25.45	25.42	23.70	26.30	P
Rdet_unpwr	>99.00	>99.00	kohm	99.00	99.00	99.00	<12.00	>45.00	P
Rdet_at_Vmin	24.98	25.02	kohm	24.98	25.02	25.00	23.70	26.30	P
Rdet_at_Vmax	25.27	25.20	kohm	25.20	25.27	25.24	23.70	26.30	P
Rdet_Voffset	1.0	1.0	VDC	1.0	1.0	1.0	0.0	1.9	P
Cdet	0.10	0.09	uF	0.09	0.10	0.10	0.05	0.12	P
Cdet_final	0.10	0.09	uF	0.09	0.10	0.10	0.05	0.12	P
1 Event Classification									
Iclass	40.1	40.1	mA	40.1	40.1	40.1	36.0	44.0	P
ClassNum	4	4		4	4	-	0	4	P
Tclass	0.0005	0.0005	sec	0.0005	0.0005	0.0005	0.0005	0.0050	P
ClassStability	1	1					1	1	P
Iclass_at_Vmin	38.9	38.8	mA	38.8	38.9	38.8	36.0	44.0	P
Iclass_at_Vmax	40.1	40.6	mA	40.1	40.6	40.4	36.0	44.0	P
2 Event Classification									
Iclass_event1	40.1	40.1	mA	40.1	40.1	40.1	36.0	44.0	P
Iclass_event2	40.1	40.1	mA	40.1	40.1	40.1	36.0	44.0	P
MarkI	2.13	2.15	mA	2.13	2.15	2.14	0.25	4.00	P
ClassNum2	4	4		4	4	-	0	4	P
Tclass_event1	0.0005	0.0005	sec	0.0005	0.0005	0.0005	0.0005	0.0050	P
Tclass_event2	0.0005	0.0005	sec	0.0005	0.0005	0.0005	0.0005	0.0050	P
ClassStability_event1	1	1					1	1	P
ClassStability_event2	1	1					1	1	P

Power-Up / Down

Parameter	Cycle: 1	2	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
InrushI_1	391.6	391.5	mA	391.5	391.6	391.6	0.0	400.0	P
InrushI_2	391.2	391.2	mA	391.2	391.2	391.2	0.0	400.0	P
IlimMinViolation	0	0		0	0	-	0	0	P
Pmax_Tdelay	2.3	2.3	W	2.3	2.3	2.3	0.0	14.4	P
Inrush_delayed	0	0		0	0	-	0	0	P
Von	36.9	36.9	VDC	36.9	36.9	36.9	30.0	42.0	P
Voff	33.5	33.4	VDC	33.4	33.5	33.5	30.0	42.0	P
Vhyst	3.3	3.4	VDC	3.3	3.4	3.4	2.8	12.0	INFO
BackfeedV	0.0	0.0	VDC	0.0	0.0	0.0	0.0	2.8	P
ClassRecover	0	0		0	0	-	0	0	P
SigRecoverTime	0.0	0.0	sec	0.0	0.0	0.0	0.0	30.0	P

MDI Powered Type-1

PSE Emulation: On Time: 20 sec Off Time: 10 sec Vport: 56.0

Parameter	Cycle: 1	2	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
MinI_1	40.2	40.2	mA	40.2	40.2	40.2	0.0	258.6	P
MaxI_1	244.3	244.4	mA	244.3	244.4	244.4	10.0	258.6	P
Vport_1	55.6	55.6	VDC	55.6	55.6	55.6	37.0	57.0	INFO
Ppeak_1	13.59	13.60	W	13.59	13.60	13.59	0.0	14.4	P
Pavg_1	12.18	12.18	W	12.18	12.18	12.18	0.0	13.0	P
MPSViolation_1	0	0		0	0	-	0	0	P
TcutWindowViolation_1	0	0		0	0	-	0	0	P
DutyCycleViolation_1	0	0		0	0	-	0	0	P

MDI Powered Type-2 PHY

PSE Emulation: On Time: 20 sec Off Time: 10 sec Vport: 43.5

Parameter	Cycle: 1	2	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
MinI_2	39.5	39.5	mA	39.5	39.5	39.5	0.0	663.7	P
MaxI_2	637.8	637.8	mA	637.8	637.8	637.8	10.0	663.7	P
Vport_2	42.6	42.6	VDC	42.6	42.6	42.6	42.5	57.0	INFO
Ppeak_2	27.17	27.19	W	27.17	27.19	27.18	0.0	28.3	P
Pavg_2	24.41	24.41	W	24.41	24.41	24.41	0.0	25.5	P
MPSViolation_2	0	0		0	0	-	0	0	P
TcutWindowViolation_2	0	0		0	0	-	0	0	P
DutyCycleViolation_2	0	0		0	0	-	0	0	P

MDI Powered Type-2 LLDP

PSE Emulation: On Time: -1 sec Off Time: 10 sec Vport: -1.0

Parameter	Cycle: 1	2	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F

NOTE: *Type-2 testing did not include LLDP, so PD Data Link Layer characteristics were not checked.*

PD Conformance Test Suite Parameters

Parameter	Description	Units	Acceptance Criteria (802.3at references)	PICS
Detection & Classification				
Rdet	Detection resistance (2.7 to 10.1 V band) from a single probe.	KΩ	23.7 to 26.3 802.3at Table 33-14	PD7 PD8 PD11
Rdet_final	Detection resistance after multiple detection and classification probing sequences. Evaluate response to PSE's that probe repeatedly prior to power-up.			
Rdet_at_Vmin **Rdet_at_Vmax**	Detection resistance at different chords in the 2.7 to 10.1 V band. (not supported by PDA-602A)	KΩ	802.3at Table 33-14	PD10 (partial)
Rdet_unpwr	Non-valid detection resistance presented by the unpowered pairset.	KΩ	< 12 or > 45 802.3at para. 33.3.4, Table 33-15	PD9
Cdet	Detection capacitance (2.7 to 10.1 V band)	μF	0.05 to 0.12 802.3at Table 33-14	PD8 PD11
Cdet_final	Detection capacitance after multiple detection and classification probing sequences. Evaluate response to PSE's that probe repeatedly prior to power-up.			
Iclass	Classification current signature, 1 event classification.	mA	0-4, 9-12, 17-20, 26-30, or 36-44	PD12 PD13 PD14 (partial)
ClassNum	PD Class determined from **Iclass**	PD Class	0, 1, 2, 3, or 4 (respectively)	
Iclass_at_Vmin **Iclass_at_Vmax**	Classification current signature at low and high edge of 14.5 - 20.5 V band. (not supported by PDA-602A)	mA	0-4, 9-12, 17-20, 26-30, or 36-44 802.3at Tables 33-16, 33-17	PD15 PD16 PD17 PD18
Tclass	Time from when **Vport** = 15.5VDC until class current reaches valid level	seconds	< 0.005 802.3at 33.3.7.8 & Table 33-18	PD19 (partial) PD20 PD21
ClassStability	Class current remains in valid range during classification period that ranges from 6-75 msec	flag (1 or 0)	All class samples = Iclass 802.3at para. 33.3.7.8	PD22 PD23 PD42
Iclass_event1	Classification current signature during 1st Classification pulse, 2-Event classification.	mA	0-4, 9-12, 17-20, 26-30, or 36-44 802.3at Table 33-16	
Iclass_event2	Classification current signature during 2nd Classification pulse, 2-Event classification.	mA		
MarkI	Current drawn during mark region of 2-Event classification.	mA	0.25 to 4 802.3at Table 33-17	
ClassNum2	PD Class determined from Iclass during 2-Event classification.	PD Class	0, 1, 2, 3, or 4	
Tclass_event1	Time from when Vport = 15.5VDC until class current reaches valid level, during 1st Classification pulse, 2-Event classification	seconds	< 0.005 802.3at 33.3.7.8 & Table 33-18	
Tclass_event2	Time from when Vport = 15.5VDC until class current reaches valid level, during 2nd Classification pulse, 2-Event classification	seconds		
ClassStability_event1	Class current remains in valid range during classification period, 1st class pulse.	flag (1 or 0)	All class samples = Iclass 802.3at para. 33.3.7.8	
ClassStability_event2	Class current remains in valid range during classification period, 2nd class pulse.	flag (1 or 0)		
Power-Up / Down				
Inrushl_1	Highest current drawn during the first 50 msec after power on. Power on is preceeded by a 1 event classification.	mA	< 400mA A PD may draw more than 400mA if it presents capacitive load, **Cport** < 180uF. Because **Cport** cannot be measured, out-of-limit performance is flagged with WARN, not FAIL. 802.3at Table 33-18 & para. 33.3.7.3.	PD30 PD31
Inrushl_2 *(Type-2 testing)*	Highest current drawn during the first 50 msec after power on. Power on is preceeded by a 2 event classification.	mA		
Pmax_Tdelay *(Type-2 testing)*	Highest power consumed during the period from 50 msec to 80 msec after power-up that is preceded by 2-Event classification.	watts	< 14.4 A Type-2 PD must not draw a peak power exceeding Type-1 PD maximum peak power of 14.4W during 30msec between end of inrush and **Tdelay**. 802.3at para. 33.3.7.3.	
Inrush_delayed	Flags a PD behavior where the start of inrush is delayed by more than 1ms after power-up and where inrush level (>400mA) may require current limiting by a PSE. PD's that delay inrush and exceed 400mA may not experience 450mA current limiting by a PSE that applies the "**legacy_powerup**" exception. See 802.3at, para. 3.2.4.4.	flag (1 or 0)	flag= 0 if; Inrush Current < 400 mA or Inrush Current (@ > 1 msec) ≤ Current (@ < 1 msec)	
IlimMinViolation	Flags a PD behavior where the DUT draws >400mA after T_{Inrush} has completed, when the PD is powered following 1 event classification. This may cause current limiting by a PSE, and the resulting reduction in voltage could cause the PD to not successfully power on. (this check was added based on an interop problem case)	flag (1 or 0)	flag= 0 if Current > 400 mA during the 1.5 second interval starting at the completion of T_{Inrush}.	PD33
Von	Voltage at which PD starts to draw load current	VDC	30 to 42, 802.3at Table 33-18	PD25
Voff	Voltage at which PD stops drawing load current	VDC	> 30, 802.3at Table 33-18. WARN if > 37VDC, based on the lowest voltage that a Type 1 PSE could provide.	PD27 PD28
Vhyst	Hysteresis band between **Von** and **Voff**.	VDC	Provided for information purposes. There is no explicit value for hysteresis specified in Table 33-18, but the need for hysteresis is implied by 802.3at para. 33.3.7.1 (*startup oscillation*).	
BackfeedV	Voltage present on the unpowered pair with V_{port_PD} max applied. Unpowered pair is terminated with 100kΩ.	VDC	0 to 2.8 802.3at Table 33-18, para. 33.3.7.9	PD43
ClassRecover	Flag indicating that PD classification signature following power removal and PD restoration of a potentially acceptable detection signature (i.e. between 15KΩ and 33KΩ) does not match expected PD classification signature.	flag (1 or 0)	A value of "1" will be reported with a WARN status because PSE's will possibly mis-classify this PD following power-removal events.	
SigRecoverTime	Time to restore expected PD classification signature if **ClassRecover** is reported as "1". Otherwise, vaue reported is "0 sec". Measured from PD shutdown.	seconds	A PD need not present a valid classification signature until that PD first presents a valid detection signature, so there is no clear specification time limit.	
MDI Powered Type-1	***PD-under-test powered to Type-1 Vport with 1-Event Classification***			
MinI_1	Minimum current drawn by the PD while powered on at Type-1 **VPort_PD**	mA	0 to (**Pclass_PD** / **VPort_PD**) 802.3at para. 33.3.7.4, Table 33-18	PD24 (partial) PD26 PD29 PD32 PD33
MaxI_1	Maximum current drawn by the PD while powered on at Type-1 **Vport_PD**. The maximum current must exceed **Iport_mps**.	mA	10 to (**Pclass_PD** / **VPort_PD**) 802.3at para. 33.3.7.4, Table 33-18 **Iport_mps** > 10, 802.3at para. 33.3.8	
Vport_1	Vport level at the point where the **MaxI_1** occurs.	V	**VPort_PD** should conform to ranges in 802.3at Table 33-18	PD34 (partial) PD35
Ppeak_1	Maximum instantaneous power consumed by the PD while powered on at Type-1 **Vport_PD**	watts	< **PPeak_PD** (for PD Classification) 802.3at Table 33-18, para. 33.3.7.4	PD37 **only if MPS low pwr
Pavg_1	Average power (1 second moving window) consumed by the PD while powered on a Type-1 **Vport_PD**	watts	< **Pclass_PD** (for PD Classification) 802.3at Table 33-18, para. 33.3.7.2	PD44
MPSViolation_1	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > 10mA for a time period > 75 msec out of every 325 msec time interval. 802.3at para. 33.3.8	PD45
TcutWindowViolation_1	Flag indicating that PD power draw exceeded **Pclass_PD** for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed **Pclass_PD** for time duration > **Tcut_min**, or 50 msec. 802.3at para. 33.3.7.4.	
DutyCycleViolation_1	Flag indicating that PD power draw is exceeding **Pclass_PD** for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed **Pclass_PD** for > 5% duty cycle. 802.3at para. 33.3.7.4.	
MDI Powered Type-2	***PD-under-test powered to Type-2 Vport with 2-Event Classification***			
MinI_2	Minimum current drawn by the PD while powered on at Type-2 **Vport_PD** prior to LLDP power allocation.	mA	0 to (25.5W / **VPort_PD**) 802.3at para. 33.3.7.4, Table 33-18	PD24 (partial) PD26 PD29 PD32 PD33
MaxI_2	Maximum current drawn by the PD while powered on at Type-2 **Vport_PD** prior to LLDP power allocation. The maximum current must exceed **Iport_mps**.	mA	10 to (25.5W / **VPort_PD**) 802.3at para. 33.3.7.4, Table 33-18 Iport_mps > 10, 802.3at para. 33.3.8	
Vport_2	Vport level at the point where the **MaxI_2** occurs.	V	**VPort_PD** should conform to ranges in 802.3at Table 33-18	PD34 (partial) PD35
Ppeak_2	Maximum instantaneous power consumed by the PD while powered on at Type-2 **VPort_PD**	watts	< 28.3 802.3at Table 33-18, para. 33.3.7.4	PD37 **only if MPS low pwr
Pavg_2	Average power (1 second moving window) consumed by the PD while powered on a Type-2 **VPort_PD**	watts	< 25.5 802.3at Table 33-18, para. 33.3.7.2	PD44
MPSViolation_2	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method.	flag (1 or 0)	In order to stay powered, the PD must draw > 10mA for a time period > 75 msec out of every 325 msec time interval. 802.3at para. 33.3.8.	PD45
TcutWindowViolation_2	Flag indicating that PD power draw exceeded 25.5W for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 25.5W for time duration > **Tcut_min**, or 50 msec. 802.3at para. 33.3.7.4.	

DutyCycleViolation_2	Flag indicating that PD power draw is exceeding 25.5W for greater than 5% of the time. PD is at risk of overload shutdown by PSE.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 25.5W for > 5% duty cycle. 802.3at para. 33.3.7.4.	
MDI Powered Type-2 LLDP	***PD-under-test powered to Type-2 Vport with 1-Event Classification and subsequent LLDP Negotiation***			
MinI_PreAlloc	Minimum current drawn by the PD while powered on at Type-2 **Vport_PD** prior to LLDP power allocation.	mA	0 to (13W / **VPort_PD**) 802.3at para. 33.3.3.5, Table 33-18	PD24 (partial) PD26 PD29 PD32 PD33 PD34 (partial) PD35 PD37 PD44 PD45 DLL4 DLL7 DLL9 DLL11
MaxI_PreAlloc	Maximum current drawn by the PD while powered on at Type-2 **Vport_PD** prior to LLDP power allocation. The maximum current must exceed **Iport_mps**.	mA	10 to (13W / **VPort_PD**) 802.3at para. 33.3.3.5, Table 33-18 **Iport_mps** > 10, 802.3at para. 33.3.8	
Vport_PreAlloc	Vport level at the point where the **MaxI_PreAlloc** occurs.	V	**VPort_PD** should conform to ranges in 802.3at Table 33-18	
Ppeak_PreAlloc	Maximum power consumed by the PD while powered on at Type-2 Vport prior to LLDP power allocation.	watts	< 14.4 802.3at Table 33-18, para. 33.3.3.5	
Pavg_PreAlloc	Average power (1 second moving window) consumed by the PD while powered on at Type-2 Vport prior to LLDP power allocation.	watts	< 25.5 802.3at Table 33-18, para. 33.3.3.5	
MPSViolation_PreAlloc	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method. Measured before LLDP power allocation.	flag (1 or 0)	In order to stay powered, the PD must draw > 10mA for a time period > 75 msec out of every 325 msec time interval. 802.3at para. 33.3.8.	
TcutWindowViolation_PreAlloc	Flag indicating that PD power draw exceeded 13W for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE. Measured before LLDP power allocation.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 13W for time duration > **Tcut_min**, or 50 msec. 802.3at para. 33.3.7.4.	
DutyCycleViolation_PreAlloc	Flag indicating that PD power draw is exceeding 13W for greater than 5% of the time. PD is at risk of overload shutdown by PSE. Measured before LLDP power allocation.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 13W for > 5% duty cycle. 802.3at para. 33.3.7.4.	
MinI_PostAlloc	Minimum current drawn by the PD while powered on at Type-2 **Vport_PD** following LLDP power allocation.	mA	0 to (**Pclass_PD** / **VPort_PD**) 802.3at para. 33.3.7.4, Table 33-18	
MaxI_PostAlloc	Maximum current drawn by the PD while powered on at Type-2 **Vport_PD** following LLDP power allocation.	mA	10 to (**Pclass_PD** / **VPort_PD**) 802.3at para. 33.3.7.4, Table 33-18 **Iport_mps** > 10, 802.3at para. 33.3.8	
Vport_PostAlloc	Vport level at the point where the **MaxI_PostAlloc** occurs.	V	**VPort_PD** should conform to ranges in 802.3at Table 33-18	
Ppeak_PostAlloc	Maximum power consumed by the PD while powered on at Type-2 **Vport_PD** following LLDP power allocation.	watts	≤ **Ppeak_PD** 802.3at Table 33-18, para. 33.3.3.5	
Pavg_PostAlloc	Average power (1 second moving window) consumed by the PD while powered on at Type-2 **Vport_PD** following LLDP power allocation.	watts	≤ **Pclass_PD** 802.3at Table 33-18, para. 33.3.3.5	
MPSViolation_PostAlloc	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method. Measured after LLDP power allocation.	flag (1 or 0)	In order to stay powered, the PD must draw > 10mA for a time period > 75 msec out of every 325 msec time interval. 802.3at para. 33.3.8.	
WindowViolation_PostAlloc	Flag indicating that PD power draw exceeded **Pclass_PD** for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE. Measured after LLDP power allocation.	flag (1 or 0)	In order to stay powered, transient loads may not exceed **Pclass_PD** for time duration > **Tcut_min**, or 50 msec. 802.3at para. 33.3.7.4.	
DutyCycleViolation_PostAlloc	Flag indicating that PD power draw is exceeding **Pclass_PD** for greater than 5% of the time. PD is at risk of overload shutdown by PSE. Measured after LLDP power allocation.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 13W for > 5% duty cycle. 802.3at para. 33.3.7.4.	
MinI_AfterThrottle	Minimum current drawn by the PD while powered on at Type-2 **Vport_PD** and following allocation reduction (or power demotion) by PSE.	mA	0 to (13W / **VPort_PD**) INFO limit - not a required behavior - 802.3at para. 33.6.3.5	
MaxI_AfterThrottle	Maximum current drawn by the PD while powered on at Type-2 **Vport_PD** and following allocation reduction (or power demotion) by PSE.	mA	10 to (13W / **VPort_PD**) INFO limit - not a required behavior - 802.3at para. 33.6.3.5 **Iport_mps** > 10, 802.3at para. 33.3.8	
Vport_AfterThrottle	Vport level at the point where the **MaxI_AfterThrottle** occurs.	V	**VPort_PD** should conform to ranges in 802.3at Table 33-18	
Ppeak_AfterThrottle	Maximum power consumed by the PD while powered on at Type-2 **Vport_PD** and following allocation reduction (or power demotion) by PSE.	watts	< 14.4 INFO limit - not a required behavior - 802.3at para. 33.6.3.5	
Pavg_AfterThrottle	Average power (1 second moving window) consumed by the PD while powered on at Type-2 **Vport_PD** and following allocation reduction (or power demotion) by PSE.	watts	< 25.5 INFO limit - not a required behavior - 802.3at para. 33.6.3.5	
MPSViolation_AfterThrottle	Flag indicating PD did not satisfy DC Maintain Power Signature (MPS) and will be potentially subject to shutdown by a PSE using DC MPS method. Measured after LLDP power demotion.	flag (1 or 0)	In order to stay powered, the PD must draw > 10mA for a time period > 75 msec out of every 325 msec time interval. 802.3at para. 33.3.8.	
TcutWindowViolation_AfterThrottle	Flag indicating that PD power draw exceeded 13W for longer than **Tcut_min** (50 msec). PD is at risk of overload shutdown by PSE. Measured after LLDP power demotion.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 13W for time duration > **Tcut_min**, or 50 msec. 802.3at para. 33.3.7.4.	
DutyCycleViolation_AfterThrottle	Flag indicating that PD power draw is exceeding 13W for greater than 5% of the time. PD is at risk of overload shutdown by PSE. Measured after LLDP power demotion.	flag (1 or 0)	In order to stay powered, transient loads may not exceed 13W for > 5% duty cycle. 802.3at para. 33.3.7.4.	
PD LLDP Protocol Parameters	***PD-under-test powered to Type-2 Vport with 1-Event Classification and subsequent LLDP Negotiation***			
TimeToLink	Length of time from power-up until LAN link was established. Reports '-1' if no link established.	seconds	Information parameter (INFO).	
LinkSpeed	Speed of the LAN link. Reports '-1' if no link established.	10 | 100 | 1000	Information parameter (INFO).	
FirstReqTime	Length of time after power-up until first LLDP Power-via-MDI request received.	seconds	Information parameter (INFO). 802.3at requires that a Type-2 PD should link and initiate LLDP within 5 minutes of power-up.	
PowerRequest	Contents of the PD requested power value field in the Power-via-MDI TLV.	watts	0.1 to 25.5 802.3 Clause 79.3.2.5	DLL2 DLL3 DLL4 DLL9 DLL11
PDAckTime	Length of time for transmission of an updated LLDPDU after an LLDPDU with a new PSE allocated power value is received by the PD.	seconds	< 10 802.3at para. 33.6.2	
AllocPowerEchoed	Contents of the PSE allocated power value from the LLDPDU acknowledgement sent by the PD.	watts	= Allocated power value sent by PDA-600 – this will be same as **PowerRequest** so long as **PowerRequest** is valid value..	
ThrottleAckTime	The length of time it takes the PD to acknowledge receiving the new alloc value.	seconds	< 10 seconds 802.3at para. 33.6.2	
ThrottlePowerReq	The PD requested power value field in the LLDPDU containing the Throttle Ack.	watts	Information parameter (INFO). A PD is not required to alter its power request after power demotion.	