PSE Conformance Test Suite
August 24 2017 6:43 PM

Port Count..... 24
Loop Count..... 1
PSE Tested: **Sample Type-2 LLDP PSE**

Sifos Technologies

802.3at Conformance Report
version 4.2.06
report version 4.2.01

Test Mode: **30 Watt LLDP**
Sifos Interop Index*: **99%**
Error Log: None

Chassis ID: 192.168.221.103
TestLoop: 1

	PSA-3000 Ports																															
	1-1	1-2	2-1	2-2	3-1	3-2	4-1	4-2	5-1	5-2	6-1	6-2	7-1	7-2	8-1	8-2	9-1	9-2	10-1	10-2	11-1	11-2	12-1	12-2	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v																																
Open_Circuit_Det_Voc=	14.4	14.07	14.1	14.18	14.07	14.28	14.43	14.3	13.68	13.78	13.85	13.98	13.95	14.03	13.98	14.45	14.07	13.98	14.23	13.98	14	13.85	13.88	13.85	volts	13.68	14.45	14.06	2.8	Pass	30	Pass
Peak_Det_Vvalid=	7.06	7.07	7.08	7.14	7.08	7.14	7.06	7.06	7.01	7.08	7.07	7.08	7.1	7.11	7.13	7.09	7.13	7.16	7.1	7.09	7.05	7.07	7.08	7.04	volts	7.01	7.16	7.1	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.96	5.01	5	5.06	5.02	5.02	4.97	4.98	4.97	5.01	5	5.03	5.05	5.05	5.05	5.02	5.05	5.08	5.04	5.03	5	5.01	5.02	4.98	volts	4.96	5.08	5	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	1.41	1.39	1.4	1.42	1.39	1.41	1.41	1.42	1.38	1.37	1.39	1.38	1.39	1.41	1.38	1.4	1.42	1.39	1.39	1.39	1.38	1.4	1.39	1.38	volts	1.37	1.42	1.4	1	Pass	7.2	Pass
Detection_Slew=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	edges	3	3	3	1	Pass	9	Pass
Backoff_Voltage=	0.5	0.5	0.5	0.5	0.5	0.5	0.4	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.6	0.6	0.6	0.5	0.5	0.5	0.5	volts	0.4	0.6	0.5	0	Pass	9	Pass
Non_802_Step_V=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	volts	0	0	0	0	Pass	0.1	Pass
High_Sig_MaxV=	11.11	11.15	11.15	11.27	11.21	11.22	11.22	11.18	11.1	11.16	11.18	11.17	11.21	11.3	11.03	11.05	11.16	11.21	11.24	11.19	11	11.03	11.03	10.96	volts	10.96	11.3	11.1	3.8	Pass	11	Info
Non_802_Discr_?=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	2	Pass
Test: det_i																																
Init_Current_Isc=	0.2	0.21	0.2	0.21	0.2	0.2	0.2	0.2	0.23	0.23	0.22	0.23	0.23	0.23	0.21	0.2	0.22	0.21	0.23	0.23	0.21	0.21	0.2	0.19	mA	0.19	0.23	0.21	0	Pass	5	Pass
Det_Current_Isc=	0.17	0.18	0.17	0.18	0.18	0.18	0.18	0.17	0.19	0.2	0.19	0.2	0.19	0.2	0.18	0.17	0.19	0.19	0.19	0.2	0.19	0.18	0.17	0.17	mA	0.17	0.2	0.18	0	Pass	5	Pass
Test: det_range																																
Rgood_Max=	29	29	29	29	29	29	29	28	29	29	29	29	28	29	29	29	29	28	29	29	29	28	29	28	Kohm	28	29	28.8	26	Pass	32	Pass
Rgood_Min=	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	17	16	16	17	17	16	16	17	17	Kohm	16	17	16.8	16	Pass	19	Pass
Rmid_det=	29	29	29	29	29	29	29	28	29	29	29	29	28	29	29	29	29	28	29	29	29	28	29	28	Kohm	28	29	28.8	26	Pass	33	Pass
Cgood_Max=	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time																																
Backoff_Time_Tdbo=	203	207	207	211	207	207	211	209	211	211	211	211	199	207	211	211	207	195	207	211	207	207	207	207	msec	195	211	207.6	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	1300	1400	1400	211	207	207	211	209	1300	1600	1500	211	2000	1300	1300	1500	207	195	1700	211	207	207	1700	207	msec	195	2000	853.8	-1	Pass	1500	Info
Backoff_Type=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	258	260	258	260	258	260	262	262	260	260	262	262	260	260	262	262	258	258	258	260	260	260	260	260	msec	258	262	260	5	Pass	500	Pass
Total_Det_Time=	266	266	268	266	270	266	266	266	266	264	266	266	266	266	268	268	264	264	268	266	266	264	270	266	msec	264	270	266.3	5	Pass	1000	Pass
Test: det_rsource																																
Output_Impedance_Zout=	350.9	450	450	450	449.5	450	450	367.1	450	450	450	450	450	426.4	330.5	450	224.5	325	450	449.5	387.4	404.8	225.1	289	KOhm	224.5	450	401.2	45	Pass	2000	Pass
Test: class_v																																
Class_Voltage_Vclass=	17.5	17.5	17.5	17.5	17.6	17.6	17.7	17.7	17.5	17.8	17.8	17.8	17.9	18	17.7	17.9	17.6	17.7	17.6	17.6	17.8	17.7	17.6	17.6	volts	17.5	18	17.7	15.5	Pass	20.5	Pass
Vclass_Min=	17.5	17.4	17.5	17.5	17.6	17.5	17.7	17.7	17.4	17.7	17.8	17.7	17.9	17.9	17.7	17.8	17.6	17.7	17.5	17.6	17.8	17.7	17.5	17.6	volts	17.4	17.9	17.6	15.5	Pass	20.5	Pass
Test: class_time																																
Event_Count=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Class_Time_Tpdc=	10.6	11.1	11.5	10.8	10.6	11.5	11.1	10.8	11.3	11.1	11.3	11.5	11.1	10.8	11.1	11.3	10.8	10.6	11.3	11.3	11.1	11.5	11.1	11.3	msec	10.6	11.5	11.1	5.6	Pass	75	Pass
Test: class_err																																
Class_lim=	78	79	79	79	79	78	78	80	78	78	79	79	79	80	78	80	76	78	79	80	75	75	75	75	mA	75	80	78	51	Pass	100	Pass
Pwr_Cl_lim=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: class_lldp																																
PSE_Source_Priority=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_MDI_Pwr_Sup=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*	0	0	*	0	Pass	0	Pass
PSE_LLDP_Time_2=	4.3	2.5	5.4	2.8	7.6	2.5	4	4.3	2.4	2.7	5.4	2.8	5.3	2.4	2.7	2.4	2.4	2.4	2.8	2.4	2.4	2.7	2.7	2.4	sec	2.4	7.6	3.3	0	Pass	10	Pass
PSE_LLDP_Type_2=	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	*	2	2	2	0	Pass	2	Pass
PSE_Echo_Time_2=	1.1	2.4	2	2.4	2	1.1	1.1	1.1	1.9	1.9	2	2	1.9	2.3	1.9	1.9	1.9	1.9	2.3	2.3	1.9	2.3	1.9	1.9	sec	1.1	2.4	1.9	0	Pass	10	Pass
PSE_Alloc_Pwr_2=	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	Watts	20.3	20.3	20.3	20.3	Pass	25.5	Pass
PSE_Alloc_Time_2=	1.1	2.4	2	2.4	2	1.1	1.1	1.1	1.9	1.9	2	2	1.9	2.3	1.9	1.9	1.9	1.9	2.3	2.3	1.9	2.3	1.9	1.9	sec	1.1	2.4	1.9	0	Pass	30	Pass
PD_Power_Adjust_2=	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	Watts	25.5	25.5	25.5	25.5	Pass	25.5	Pass
PSE_Adjust_Time_2=	1.5	2	2	1.5	2	1.5	1.5	1.5	1.9	1.1	2	2	2.3	1.9	2.3	1.1	1.9	2.3	1.9	1.9	2.3	1.9	2.3	1.9	sec	1.1	2.3	1.9	0	Pass	10	Pass
Link_Down_Shutdown_?=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		0	0	0	0	Pass	0	Pass
Test: pwrup_time																																
Pwr-On_Rise_Time_Trise=	20	21	20	20	20	21	20	20	58	58	21	23	57	47	60	61	65	66	22	22	65	66	63	66	usec	20	66	41	15	Pass	50000	Pass
Power-On_Time_Tpon=	43	39.1	226.6	39.1	308.6	375	328.1	230.5	171.9	35.1	304.7	214.8	218.8	66.4	117.2	31.3	39.1	39.1	261.7	39.1	39.1	351.6	39.1	35.1	msec	31.3	375	149.8	0	Pass	400	Pass
Test: pwrup_inrush																																
Init_Iinrush=	438.13	441	443.5	446.13	443.13	442.88	443.75	444.75	441.38	439.38	444	441.25	442.38	445	437.88	440.5	437.5	442.88	437.25	445.63	436	441.13	443.13	442.88	mA	436	446.13	441.7	400	Pass	450	Pass
Max_Iinrush_c4=	442.38	442.63	443.38	445.5	443.38	444.38	443.75	445.13	443.13	443.25	444.38	444.13	443.5	445.25	441.5	444.75	443.75	444	440	445.25	438.88	442.25	441.5	444.25	mA	438.88	445.5	443.3	400	Pass	450	Pass
Min_Iinrush=	434.5	433.75	438	438.75	437.75	436.25	435.88	437.13	436.5	433.75	438.5	434.63	437.75	438.25	435	436.88	433.13	436.5	433.13	437.63	431.88	434.13	435.88	436.38	mA	431.88	438.75	435.9	400	Pass	450	Pass
Tinrush=	65	65	65	65	65	65	65	65.4	65	65	65.4	65.4	65	65	65.4	65.4	64.6	64.6	65	65	63.9	57.2	64.3	64.3	msec	57.2	65.4	64.6	50	Pass	75	Pass
Inrush_45m=	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	56.1	55.9	56	56.2	56.1	56.1	56.1	56	56.1	Volts	55.9	56.2	56.1	50	Pass	57	Pass
Inrush_Voltage=	31.4	31.5	31.6	31.7	31.7	31.4	31.6	31.6	31.7	31.5	31.6	31.6	32.2	32.2	31.3	31.5	31.3	31.4	32.1	32.2	35.5	35.6	35.8	35.8	Volts	31.3	35.8	32.3	30	Pass	57	Pass
Max_Init_Inrush=	678.3	714	797.5	777.3	796.3	802.8	713	718	440.8	438.5	566.3	509.8	442.5	447	437	440.3	437	443.5	503	504.3	662	631.3	636.3	650.5	mA	437	802.8	591.1	0	Pass	2000	Pass
Inrush_Strategy_c4=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v																																
Vport_min_2=	55.5	55.5	55.5	55.6	55.6	55.6	55.6	55.5	55.5	55.5	55.6	55.6	55.6	55.6	55.5	55.5	55.3	55.4	55.6	55.5	55.5	55.5	55.4	55.5	V	55.3	55.6	55.5	50	Pass	57	Pass
Vport_max_2=	56.2	56.2	56.2	56.2	56.3	56.2	56.2	56.2	56.2	56.2	56.2	56.2	56.3	56.3	56.2	56.2	56	56.1	56.3	56.2	56.2	56.2	56.1	56.1	V	56	56.3	56.2	50	Pass	57	Pass
Vport_ripple_2=	11	9	11	11	11	11	11	9	11	12	11	11	11	11	11	11	11	9	11	11	11	12	12	11	mVpp	9	12	10.9	0	Pass	500	Pass
Vport_noise_2=	37	34	37	33	37	33	37	35	32	35	33	36	35	36	29	32	32	34	35	34	31	31	31	31	mVpp	29	37	33.8	0	Pass	200	Pass
Vtrans_min_2=	55.4	55.4	55.5	55.5	55.5	55.5	55.5	55.5	55.4	55.5	55.5	55.5	55.5	55.5	55.4	55.5	55.2	55.4	55.5	55.5	55.5	55.4	55.3	55.5	V	55.2	55.5	55.5	50	Pass	57	Pass
Vtrans_max_2=	56.2	56.3	56.2	56.2	56.3	56.3	56.2	56.3	56.2	56.2	56.2	56.3	56.3	56.3	56.2	56.2	56	56.1	56.3	56.3	56.2	56.2	56.1	56.2	V	56	56.3	56.2	50	Pass	57	Pass
Test: pwron_pwrcap																																
Pcon_c4=	34.2	34.1	34.1	34.1	34	34.1	34.1	34.2	34.1	34.2	34.2	34.1	34	34.1	34.1	34.1	34.2	34.2	34.1	34.2	34.1	34.1	34.1	34.2	watts	34	34.2	34.1	28.7	Pass	38.9	Pass
Icon_%_c4=	118.5	118.2	118.2	118.2	118	118.3	118.2	118.6	118.1	118.3	118.4	118.2	118	118.3	118.2	118.3	118.2	118.4	118.1	118.4	118.3	118	118	118.4	%	118	118.6	118.2	100	Pass	9999	Pass
Type-2_Enable=	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pclass_LLDP_22.7=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Pclass_LLDP_24.5=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi																																
Ilim_Peak=	79	79.5	78.5	79.5	81.5	79.5	78.5	78.5	79	79	80.5	78.5	78	81	80.5	81	78.5	81	80.5	80	78.5	78.5	79	80	mA	78	81.5	79.5	0	Pass	1750	Pass
Ilim_Min_2=	687.7	686.4	684.5	685.3	684	686.5	686	687.8	685.8	685.7	687.3	685.8	684.5	686.8	687	687	686.4	687.3	684.9	686.5	685.6	684.5	686	686.8	mA	684	687.8	686.1	683	Pass	1750	Pass
Tlim_2=	63.7	63.3	64.8	64.1	63.7	64.8	62.9	63.7	63.7	62.9	65.6	64.5	64.8	63.7	65.2	64.8	62.9	62.9	63.3	64.1	62.9	64.1	63.7	65.2	msec	62.9	65.6	64	10	Pass	75	Pass
Vlim_2=	55.2	55.2	55.3	55.3	55.3	55.3	55.3	55.3	55.2	55.3	55.3	55.3	55.3	55.3	55.2	55.3	55	55.2	55.3	55.3	55.3	55.2	55.1	55.2	V	55	55.3	55.3	50	Pass	57	Pass
Ilim_Max_2=	799.8	802.8	806.8	812	805.8	807.8	811.3	810	808.3	805.8	809.3	803.8	810.3	808	800.3	804.5	808	810.8	799.5	811.5	798.5	806.3	811	808.8	mA	798.5	812	806.7	0	Pass	1750	Pass
Ilim_Low_V_Tol_2=	11.3	11.3	11.3	11.3	11.3	11.3	11.3	11.7	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.9	10.6	10.9	10.9	msec	10.6	11.7	11	10	Pass	9999	Pass
Ktran_lo_2=	110.2	110.2	110.6	110.6	110.6	110.6	110.6	110.5	110.4	110.5	110.6	110.7	110.6	110.6	110.4	110.6	110	110.3	110.5	110.5	110.5	110.5	110.2	110.4	%	110	110.7	110.5	92.4	Pass	115	Pass
Test: pwron_overld																																
%Ipeak_2=	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_2=	55.3	55.3	55.4	55.4	55.5	55.4	55.4	55.4	55.4	55.4	55.4	55.4	55.4	55.4	55.4	55.4	55.1	55.3	55.4	55.4	55.4	55.4	55.2	55.3	V	55.1	55.5	55.4	50	Pass	57	Pass
Vport_%DC_2=	55.3	55.4	55.4	55.5	55.5	55.4	55.5	55.4	55.4	55.4	55.4	55.5	55.5	55.4	55.4	55.4	55.1	55.3	55.4	55.4	55.4	55.4	55.3	55.4	V	55.1	55.5	55.4	50	Pass	57	Pass
Test: mps_dc_valid																																
Min_Valid_Time_Tmps=	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	50	msec	50	50	50	1	Pass	60	Pass
Duty_Cycle_tol=	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn																																
Min_Valid_I_hold=	9	8	9	7	7	9	7	8	8	7	8	8	7	8	8	9	9	9	8	9	8	9	7	8	mA	7	9	8.1	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	355	355	355	355	355	355	355	359	355	355	359	359	355	355	359	359	359	355	355	355	355	355	355	359	msec	355	359	356	300	Pass	400	Pass
Max_Voltage_Vopen_max=	14	0.7	14.3	13.9	14.1	13.2	14.1	0.7	0.7	0.7	0.6	13	0.7	0.7	14	0.7	13.8	0.7	14.1	0.7	0.7	13.3	0.7	14.2	volts	0.6	14.3	7.3	-1	Pass	30	Pass
Test: pwrdn_overld																																
Icut_2=	630	630	630	630	630	630	630	630	630	630	630	619.5	630	630	630	630	630	619.5	630	630	630	630	630	630	mA	619.5	630	629.1	-1	Pass	1750	Pass
Tcut_2=	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	75	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	70.3	msec	70.3	75	70.5	10	Pass	9999	Pass
Isoft_2=	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrdn_time																																
Turn-Off_Time_Toff=	25.3	24.8	25.3	25.3	25.5	25.2	24.3	24.9	25.7	25.6	26.6	26	25.4	25.2	25.6	25.8	25.7	25.8	25.4	25.2	25.3	25.7	25.9	25.6	mSec	24.3	26.6	25.5	0	Pass	500	Pass
Output_Cap_Cout=	0.1446	0.1315	0.141	0.1425	0.153	0.1436	0.1353	0.1303	0.1434	0.1454	0.1579	0.1458	0.1466	0.1459	0.1665	0.1664	0.1556	0.1551	0.1512	0.1398	0.1461	0.1538	0.155	0.1465	uF	0.1303	0.1665	0.15	-1	Pass	0.52	Pass
Output_Load_Rp=	74.7	81.9	77	75.8	70.1	74.9	76.9	83.1	76.7	74.9	71.1	76.2	73.6	73.3	63.8	64.4	69.5	70.2	71	77.3	73.6	70.5	70.6	74.5	Kohm	63.8	83.1	74	45	Pass	50000	Pass
Test: pwrdn_v																																
Avg_Idle_Voff=	0.5	0.5	0.4	0.5	0.5	0.4	0.4	0.4	0.5	0.5	0.4	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	VDC	0.4	0.5	0.5	0	Pass	2.8	Pass
Error_Delay_Ted=	1468.8	1914.1	2007.8	1578.1	2562.5	2187.5	1945.3	1515.6	2289.1	2007.8	1609.4	1929.7	2570.3	2156.3	1937.5	1968.8	1906.3