PSE Conformance Test Suite

March 23 2017 9:45 PM

Port Count..... 4
Loop Count..... 1
PSE Tested: **Sample 4-Port Type-2 2-Event PSE**

Sifos Technologies

802.3at Conformance Report
version 4.2.00
report version 4.2.00

Test Mode: **30 Watt PHY**
Sifos Interop Index*: **100%**
Error Log: None

Chassis ID: 192.168.221.108	PSA-3000 Ports											
TestLoop: 1	5-2	7-1	7-2	9-2	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v												
Open_Circuit_Det_Voc=	19.75	19.63	19.8	19.95	volts	19.63	19.95	19.78	2.8	Pass	30	Pass
Peak_Det_Vvalid=	7.18	7.12	7.12	7.13	volts	7.12	7.18	7.1	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.6	4.55	4.56	4.57	volts	4.55	4.6	4.6	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	2.58	2.57	2.56	2.56	volts	2.56	2.58	2.6	1	Pass	7.2	Pass
Detection_Slew=	0	0	0	0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	3	3	3	3	edges	3	3	3	1	Pass	9	Pass
Backoff_Voltage=	0.4	0.3	0.5	0.3	volts	0.3	0.5	0.4	0	Pass	2.8	Pass
Non_802_Step_V=	0	0	0	0	volts	0	0	0	0	Pass	0.1	Pass
High_Sig_MaxV=	**11.49**	**11.34**	**11.34**	**11.38**	volts	11.34	11.49	11.4	3.8	Pass	11	Info
Non_802_Discr_?=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	0	0	0	****	0	0	0	0	Pass	2	Pass
Test: det_i												
Init_Current_Isc=	0.19	0.19	0.15	0.18	mA	0.15	0.19	0.18	0	Pass	5	Pass
Det_Current_Isc=	0.22	0.2	0.19	0.21	mA	0.19	0.22	0.21	0	Pass	5	Pass
Test: det_range												
Rgood_Max=	28	29	28	29	Kohm	28	29	28.5	26	Pass	32	Pass
Rgood_Min=	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=	28	29	28	29	Kohm	28	29	28.5	26	Pass	33	Pass
Cgood_Max=	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time												
Backoff_Time_Tdbo=	86	94	86	86	msec	86	94	88	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	86	94	86	86	msec	86	94	88	-1	Pass	1500	Pass
Backoff_Type=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=	336	316	320	318	msec	316	336	322.5	5	Pass	500	Pass
Total_Det_Time=	338	320	332	322	msec	320	338	328	5	Pass	1000	Pass
Test: det_rsource												
Output_Impedance_Zout=	450	450	450	450	KOhm	450	450	450	45	Pass	2000	Pass
Test: class_v												
Class_Voltage_Vclass=	18.6	18.5	18.6	18.8	volts	18.5	18.8	18.6	15.5	Pass	20.5	Pass
Vclass_Min=	18.5	18.5	18.6	18.8	volts	18.5	18.8	18.6	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	8.3	8.4	8.3	8.4	volts	8.3	8.4	8.4	7	Pass	10	Pass
Mark_Voltage_Min=	8.3	8.3	8.3	8.4	volts	8.3	8.4	8.3	7	Pass	10	Pass
Test: class_time												
Event_Count=	2	2	2	2	****	2	2	2	2	Pass	3	Pass
Event1_Tcle1=	10.1	10.8	10.1	10.8	msec	10.1	10.8	10.5	6	Pass	30	Pass
Event2_Tcle2=	10.8	10.8	10.8	10.8	msec	10.8	10.8	10.8	6	Pass	30	Pass
Mark_Tme1=	7.7	7	7.7	7	msec	7	7.7	7.4	6	Pass	12	Pass
Mark_Tme2=	8.5	8.5	7.7	8.5	msec	7.7	8.5	8.3	6	Pass	376	Pass
Test: class_err												
Class_lim=	79	78	78	78	mA	78	79	78	51	Pass	100	Pass
Pwr_Cl_lim=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	6	6	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Treset=	100	98	96	92	msec	92	100	96.5	15	Pass	10000	Pass
Test: pwrup_time												
Pwr-On_Rise_Time_Trise=	40	42	36	42	usec	36	42	40	15	Pass	50000	Pass
Power-On_Time_Tpon=	35.2	39.1	39.1	35.1	msec	35.1	39.1	37.1	0	Pass	400	Pass
Test: pwrup_inrush												
Init_Iinrush=	424	420.25	421.5	422.75	mA	420.25	424	422.1	400	Pass	450	Pass
Max_Iinrush_c4=	423.5	419.75	421.13	423	mA	419.75	423.5	421.8	400	Pass	450	Pass
Min_Iinrush=	422.75	419	420.25	422.5	mA	419	422.75	421.1	400	Pass	450	Pass
Tinrush=	60.4	60.4	60	60	msec	60	60.4	60.2	50	Pass	75	Pass
Inrush_45m=	53.3	53.3	53.3	53.3	Volts	53.3	53.3	53.3	50	Pass	57	Pass
Inrush_Voltage=	31.6	30.8	30.7	31.4	Volts	30.7	31.6	31.1	30	Pass	57	Pass
Max_Init_Inrush=	561	542	561.3	504.3	mA	504.3	561.3	542.2	0	Pass	2000	Pass
Inrush_Strategy_c4=	0	0	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v												
Vport_min_2=	52.6	52.7	52.6	52.7	V	52.6	52.7	52.7	50	Pass	57	Pass
Vport_max_2=	53.5	53.5	53.4	53.5	V	53.4	53.5	53.5	50	Pass	57	Pass
Vport_ripple_2=	8	7	8	6	mVpp	6	8	7.3	0	Pass	500	Pass
Vport_noise_2=	37	37	34	35	mVpp	34	37	35.8	0	Pass	200	Pass
Vtrans_min_2=	52.5	52.7	52.5	52.6	V	52.5	52.7	52.6	50	Pass	57	Pass
Vtrans_max_2=	53.5	53.5	53.5	53.5	V	53.5	53.5	53.5	50	Pass	57	Pass
Test: pwron_pwrcap												
Pcon_c4=	32.6	32.6	32.7	32.7	watts	32.6	32.7	32.7	28.7	Pass	38.9	Pass
Icon_%_c4=	111	111.1	111.2	111.2	%	111	111.2	111.1	100	Pass	9999	Pass
Type-2_Enable=	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi												
Ilim_Peak=	99.8	97	98.8	99.3	mA	97	99.8	98.7	0	Pass	1750	Pass
Ilim_Min_2=	685.3	684.4	686.5	686.5	mA	684.4	686.5	685.7	683	Pass	1750	Pass
Tlim_2=	62.1	61.3	62.1	61.3	msec	61.3	62.1	61.7	10	Pass	75	Pass
Vlim_2=	52.4	52.5	52.3	52.5	V	52.3	52.5	52.4	50	Pass	57	Pass
Ilim_Max_2=	959	987.5	986.3	967.8	mA	959	987.5	975.2	0	Pass	1750	Pass
Ilim_Low_V_Tol_2=	61.3	61.3	62.1	60.2	msec	60.2	62.1	61.2	10	Pass	9999	Pass
Ktran_lo_2=	104.7	105	104.6	104.8	%	104.6	105	104.8	92.4	Pass	115	Pass
Test: pwron_overld												
%Ipeak_2=	125	125	125	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_2=	52.4	52.6	52.4	52.5	V	52.4	52.6	52.5	50	Pass	57	Pass
Vport_5%DC_2=	52.5	52.6	52.4	52.5	V	52.4	52.6	52.5	50	Pass	57	Pass
Test: mps_dc_valid												
Min_Valid_Time_Tmps=	50	50	50	50	msec	50	50	50	1	Pass	60	Pass
Duty_Cycle_tol=	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn												
Min_Valid_I_hold=	8	8	8	8	mA	8	8	8	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	363	364	363	363	msec	363	364	363	300	Pass	400	Pass
Max_Voltage_Vopen_max=	19.7	19.6	19.8	19.9	volts	19.6	19.9	19.8	-1	Pass	30	Pass
Test: pwrdn_overld												
Icut_2=	648	648	648	648	mA	648	648	648	-1	Pass	1750	Pass
Tcut_2=	63.9	63.3	63.4	62.6	msec	62.6	63.9	63.3	10	Pass	9999	Pass
Isoft_2=	-1	-1	-1	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	-1	-1	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrdn_time												
Turn-Off_Time_Toff=	23.4	23	23.2	23.8	mSec	23	23.8	23.4	0	Pass	500	Pass
Output_Cap_Cout=	0.0989	0.0924	0.0935	0.1018	uF	0.0924	0.1018	0.1	-1	Pass	0.52	Pass
Output_Load_Rp=	112.6	120.8	120	110.3	Kohm	110.3	120.8	116	45	Pass	50000	Pass
Test: pwrdn_v												
Avg_Idle_Voff=	0.1	0.1	0	0.1	VDC	0	0.1	0.1	0	Pass	2.8	Pass
Error_Delay_Ted=	1382.8	1375	1367.2	1367.2	msec	1367.2	1382.8	1373	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	0.4	0.4	0.3	0.3	VDC	0.3	0.4	0.4	0	Pass	20.5	Pass
Test Port Model Number:	**3202**	**3202**	**3202**	**3102**								
Test Port Hardware Version:	**8**	**8**	**8**	**8**								
Test Port Firmware Version:	**4.08**	**4.08**	**4.08**	**3.22**								