PSE Conformance Test Suite

April 12 2017 8:58 PM

Port Count..... 4

Loop Count..... 1

PSE Tested: **Sample Type-2 2-Event PSE**

Sifos Technologies

802.3at Conformance Report

version 4.2.00

report version 4.2.00

Test Mode: **30 Watt PHY**

Sifos Interop Index*: **90%**

Error Log: None

Chassis ID: 192.168.221.103	PSA-3000 Ports											
TestLoop: 1	1-1	1-2	3-1	3-2	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
Test: det_v												
Open_Circuit_Det_Voc=	4.33	4.33	4.33	4.3	volts	4.3	4.33	4.32	2.8	Pass	30	Pass
Peak_Det_Vvalid=	6.6	6.6	6.64	6.6	volts	6.6	6.64	6.6	3.8	Pass	10	Pass
Min_Det_Vvalid=	4.61	4.59	4.63	4.6	volts	4.59	4.63	4.6	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	1.99	2.01	2.01	2	volts	1.99	2.01	2	1	Pass	7.2	Pass
Detection_Slew=	0	0	0	0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	1	1	1	1	edges	1	1	1	1	Pass	9	Pass
Backoff_Voltage=	0.1	0.1	0.1	0.1	volts	0.1	0.1	0.1	0	Pass	2.8	Pass
Non_802_Step_V=	**1.98**	**1.98**	**1.98**	**1.97**	volts	1.97	1.98	2	0	Pass	0.1	Info
High_Sig_MaxV=	8.73	8.72	8.78	8.75	volts	8.72	8.78	8.7	3.8	Pass	11	Pass
Non_802_Discr_?=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	1	1	1	1	****	1	1	1	0	Pass	2	Pass
Test: det_i												
Init_Current_Isc=	0.29	0.29	0.3	0.29	mA	0.29	0.3	0.29	0	Pass	5	Pass
Det_Current_Isc=	0.26	0.26	0.27	0.27	mA	0.26	0.27	0.27	0	Pass	5	Pass
Test: det_range												
Rgood_Max=	**37**	**37**	**37**	**37**	Kohm	37	37	37	26	Pass	32	Fail
Rgood_Min=	**11**	**11**	**11**	**11**	Kohm	11	11	11	16	Fail	19	Pass
Rmid_det=	**37**	**37**	**37**	**37**	Kohm	37	37	37	26	Pass	33	Fail
Cgood_Max=	**11.1**	**11.1**	**11.1**	**11.1**	uF	11.1	11.1	11.1	0	Pass	10	Fail
Rbad_Cbad_Stat=	**1**	**1**	**1**	**1**	****	1	1	1	0	Pass	0	Fail
Test: det_time												
Backoff_Time_Tdbo=	703	703	703	707	msec	703	707	704	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=	703	703	703	707	msec	703	707	704	-1	Pass	1500	Pass
Backoff_Type=	**2**	**2**	**2**	**2**	****	2	2	2	0	Pass	0	Info
Detection_Time_Tdet=	143	143	143	145	msec	143	145	143.5	5	Pass	500	Pass
Total_Det_Time=	389	389	389	389	msec	389	389	389	5	Pass	1000	Pass
Test: det_rsource												
Output_Impedance_Zout=	**42.4**	**44.4**	**43.5**	**43.5**	KOhm	42.4	44.4	43.5	45	Info	2000	Pass
Test: class_v												
Class_Voltage_Vclass=	17.9	17.9	18.1	18.1	volts	17.9	18.1	18	15.5	Pass	20.5	Pass
Vclass_Min=	17.9	17.9	18.1	18.1	volts	17.9	18.1	18	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	8.6	8.6	8.5	8.4	volts	8.4	8.6	8.5	7	Pass	10	Pass
Mark_Voltage_Min=	8.6	8.6	8.4	8.4	volts	8.4	8.6	8.5	7	Pass	10	Pass
Test: class_time												
Event_Count=	2	2	2	2	****	2	2	2	2	Pass	3	Pass
Event1_Tcle1=	25.3	27.2	25.3	27.2	msec	25.3	27.2	26.3	6	Pass	30	Pass
Event2_Tcle2=	27.2	25.3	25.3	25.3	msec	25.3	27.2	25.8	6	Pass	30	Pass
Mark_Tme1=	7.8	7.8	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	12	Pass
Mark_Tme2=	180.9	180.9	204.3	202.4	msec	180.9	204.3	192.1	6	Pass	376	Pass
Test: class_err												
Class_lim=	69	69	69	70	mA	69	70	69	51	Pass	100	Pass
Pwr_Cl_lim=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	6	6	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	**1**	**1**	**1**	**1**	****	1	1	1	0	Pass	0	Fail
Treset=	474	473	474	473	msec	473	474	473.5	15	Pass	10000	Pass
Test: pwrup_time												
Pwr-On_Rise_Time_Trise=	**9**	**9**	**9**	**9**	usec	9	9	9	15	Fail	50000	Pass
Power-On_Time_Tpon=	253.9	253.9	269.5	273.4	msec	253.9	273.4	262.7	0	Pass	400	Pass
Test: pwrup_inrush												
Init_Iinrush=	415	410.38	422.13	423.88	mA	410.38	423.88	417.8	400	Pass	450	Pass
Max_Iinrush_c4=	412.13	407.88	419.88	421.5	mA	407.88	421.5	415.3	400	Pass	450	Pass
Min_Iinrush=	409.25	405	417	418.88	mA	405	418.88	412.5	400	Pass	450	Pass
Tinrush=	66.2	66.2	66.2	66.6	msec	66.2	66.6	66.3	50	Pass	75	Pass
Inrush_45m=	55.9	56	55.9	55.9	Volts	55.9	56	55.9	50	Pass	57	Pass
Inrush_Voltage=	31.1	30.9	31.3	31.4	Volts	30.9	31.4	31.2	30	Pass	57	Pass
Max_Init_Inrush=	957.5	922.8	941.3	956	mA	922.8	957.5	944.4	0	Pass	2000	Pass
Inrush_Strategy_c4=	0	0	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwron_v												
Vport_min_2=	55.1	55.2	55.1	55.2	V	55.1	55.2	55.2	50	Pass	57	Pass
Vport_max_2=	56.1	56.1	56.1	56	V	56	56.1	56.1	50	Pass	57	Pass
Vport_ripple_2=	53	53	54	53	mVpp	53	54	53.3	0	Pass	500	Pass
Vport_noise_2=	14	15	12	9	mVpp	9	15	12.5	0	Pass	200	Pass
Vtrans_min_2=	54.8	54.9	54.8	54.9	V	54.8	54.9	54.9	50	Pass	57	Pass
Vtrans_max_2=	56.2	56.2	56.2	56.1	V	56.1	56.2	56.2	50	Pass	57	Pass
Test: pwron_pwrcap												
Pcon_c4=	34.1	32.6	32.7	34.1	watts	32.6	34.1	33.4	28.7	Pass	38.9	Pass
Icon_%_c4=	117.9	112.9	112.9	117.9	%	112.9	117.9	115.4	100	Pass	9999	Pass
Type-2_Enable=	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: pwron_maxi												
Ilim_Peak=	72.8	71.8	70.3	70.3	mA	70.3	72.8	71.3	0	Pass	1750	Pass
Ilim_Min_2=	686.3	686	686.9	686.5	mA	686	686.9	686.4	683	Pass	1750	Pass
Tlim_2=	67.6	67.2	68	67.2	msec	67.2	68	67.5	10	Pass	75	Pass
Vlim_2=	54.7	54.8	54.8	54.8	V	54.7	54.8	54.8	50	Pass	57	Pass
Ilim_Max_2=	708.8	703.3	704.8	708.5	mA	703.3	708.8	706.4	0	Pass	1750	Pass
Ilim_Low_V_Tol_2=	57.4	58.2	58.2	57.8	msec	57.4	58.2	57.9	10	Pass	9999	Pass
Ktran_lo_2=	109.2	109.3	109.3	109.4	%	109.2	109.4	109.3	92.4	Pass	115	Pass
Test: pwron_overld												
%Ipeak_2=	116.9	116.9	116.9	116.9	%	116.9	116.9	116.9	100	Pass	125	Pass
Vport_Ipeak_2=	54.9	55	55	55	V	54.9	55	55	50	Pass	57	Pass
Vport_5%DC_2=	54.9	55	55	55	V	54.9	55	55	50	Pass	57	Pass
Test: mps_dc_valid												
Min_Valid_Time_Tmps=	50	50	50	50	msec	50	50	50	1	Pass	60	Pass
Duty_Cycle_tol=	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrdn												
Min_Valid_I_hold=	5	5	5	5	mA	5	5	5	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	326	327	324	326	msec	324	327	326	300	Pass	400	Pass
Max_Voltage_Vopen_max=	0.1	0.1	0.1	0.1	volts	0.1	0.1	0.1	-1	Pass	30	Pass
Test: pwrdn_overld												
Icut_2=	630	619.5	630	630	mA	619.5	630	627.4	-1	Pass	1750	Pass
Tcut_2=	67	67.7	67.2	67.8	msec	67	67.8	67.4	10	Pass	9999	Pass
Isoft_2=	-1	-1	-1	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	-1	-1	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrdn_time												
Turn-Off_Time_Toff=	4.8	5.4	4.8	5.4	mSec	4.8	5.4	5.1	0	Pass	500	Pass
Output_Cap_Cout=	0.0334	0.0365	0.0332	0.0379	uF	0.0332	0.0379	0.04	-1	Pass	0.52	Pass
Output_Load_Rp=	59	60.3	59.7	58.5	Kohm	58.5	60.3	59	45	Pass	50000	Pass
Test: pwrdn_v												
Avg_Idle_Voff=	0.1	0.1	0.1	0.1	VDC	0.1	0.1	0.1	0	Pass	2.8	Pass
Error_Delay_Ted=	2046.9	1546.9	2031.3	1539.1	msec	1539.1	2046.9	1791	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	2	2	2	2	VDC	2	2	2	0	Pass	20.5	Pass
Test Port Model Number:	**3202**	**3202**	**3202**	**3202**								
Test Port Hardware Version:	**8**	**8**	**8**	**8**								
Test Port Firmware Version:	**4.09**	**4.09**	**4.09**	**4.09**								